

REVISION HISTORY

[illegible]

CONNECTORS

REF	TYPE	DESCRIPTION	PAGE
CN1	DRSP	MOUSE/JOYSTICK 1	5
CN2	DRSP	MOUSE/JOYSTICK 2	5
CN3	RCR-1	RGB AUDIO INPUT	5
CN4	RCR-2	RGB AUDIO OUTPUT	5
CN5	DR235	EXTERNAL FLOPPY	6
CN6	DR25P	S232 SERIAL PORT	7
CN7	DR25P	PARALLEL PRINTER PORT	7
CN8	50 DIN	POWER SUPPLY CONNECTION	7
CN9	DR235	INTERNAL FLOPPY	8
CN10	RCR-3	COMPOSITE VIDEO	8
CN11	DIL-34	INTERNAL FLOPPY SIGNALS	8
CN12	SIL-4	INTERNAL FLOPPY POWER	8
CN13	MEM-30	KEYBOARD MEMORANDUM	9
CN14	SIL-4	KEYBOARD POWER	9
CN15	MEM-30	KEYBOARD MEMORANDUM	9
CN16	SIL-4	KEYBOARD STATUS LEDs	9
CN15	PHCIA	PC MEMORY CARD*	11
P8		EDGE-BACKBORY BUS EXPANSION	12

[illegible]

SIGNAL GLOSSARY

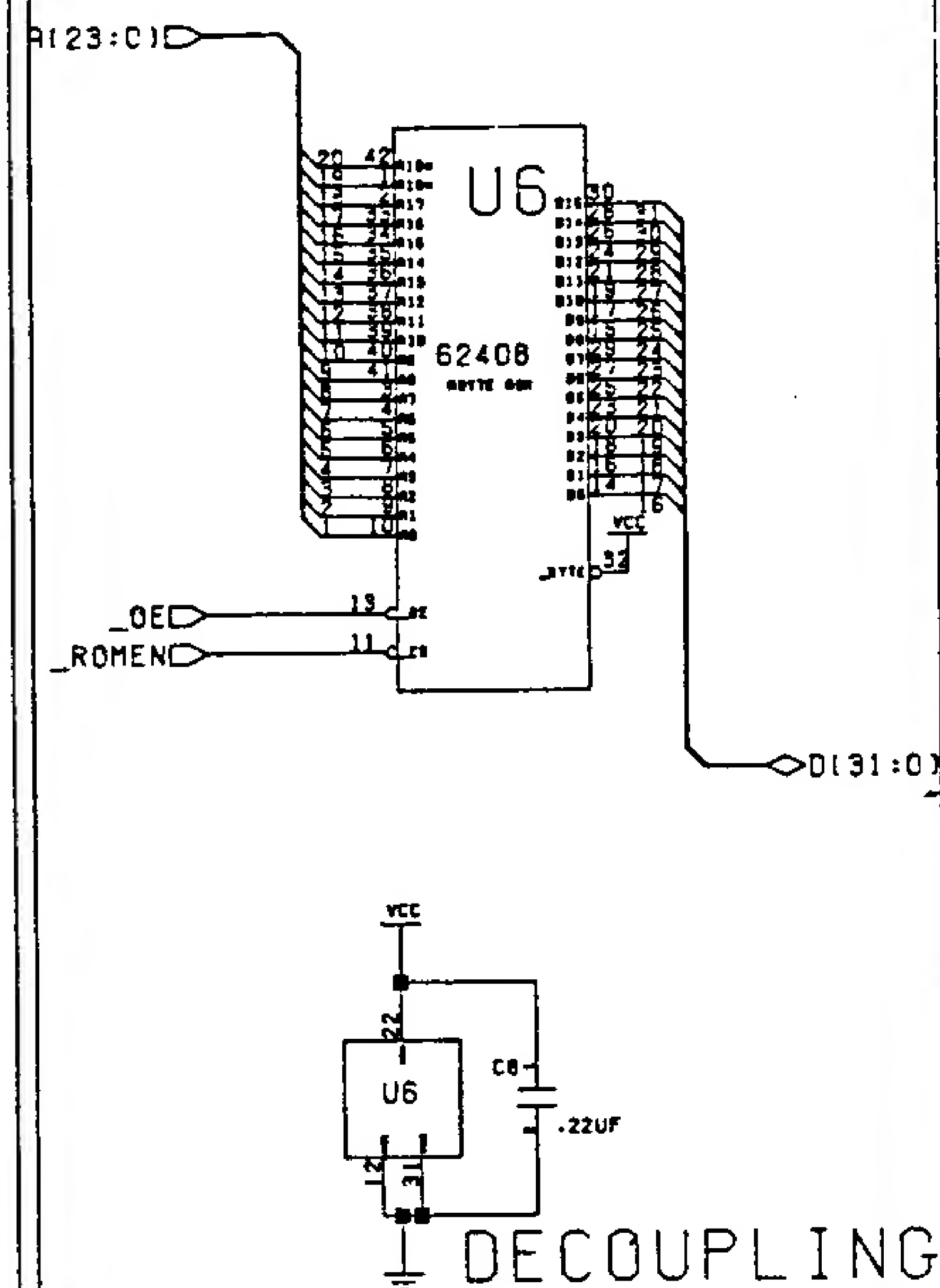
SIGNAL	DESCRIPTION (AREA)	PAGES
28MHz	28.63636 MHZ MASTER CLOCK	
7MHz	7.153909 MHZ PROCESSOR CLOCK	
A(23:1)	PROCESSOR ADDRESS BUS (160000)	
ACK	DATA ACKNOWLEDGE (PARALLEL PORT)	
AS	ADDRESS STROBE (160000)	
AUDIN	AUDIO INPUT (RS232 PORT)	
AUDOUT	AUDIO OUTPUT (RS232 JACK)	
BEER	BUS ERROR (160000)	
BS	BUS GRANT (160000)	
BRACK	BUS GRANT ACKNOWLEDGE (160000)	
BU155	BUFFER SLOWDOWN (CHIPS)	
CBIT	CHIP MEMORY ACCESS (CHIPS)	
CB	BUS REQUEST (160000)	
CBUSY	DEVICE BUSY (PARALLEL PORT)	
CAS1/0	COLUMN ADDRESS STROBE (DRAM)	
CLK/CCKO	CLOCK CLOCK / QUADRATURE (CHIPS)	
CLKC	7.153909 MHZ QUADRATURE CLOCK (CHIPS)	
CNNG	MEDIA CHANGE (160000)	
CLOCK/WR	READ / WRITE (RTC)	
CMD	MONOCHROME COMPOSITE VIDEO (VIDEO)	
CSYNC	COMPOSITE SYNC (VIDEO)	
CTS	CLEAR TO SEND (RS232 PORT)	
D(15:0)	PROCESSOR DATA BUS (160000)	
DIR	STEP DIRECTION (FLOPPY)	
DRD	DISK READ DATA (FLOPPY)	
DRW	DISK WRITE DATA (FLOPPY)	
DKWE	DISK WRITE ENABLE (FLOPPY)	
DMPL	CHIP DMA REQUEST LINE (CHIPS)	
DRAM(8:0)	DRAM ADDRESS BUS (DRAM)	
DRD(15:0)	DRAM DATA BUS (DRAM)	
DSR	DATA SET READY (RS232 PORT)	
DSACK	DATA TRANSFER ACKNOWLEDGE (160000)	
DTR	DATA TERMINAL READY (RS232 PORT)	
E	PERIPHERAL ENABLE CLOCK (160000)	
EXTICK	EXPANSION PRESENT / RTC TICK	
EX(12:0)	FUNCTION CODE (160000)	
FRDZ1	FRONT BUTTON (KEYPADS)	
HW	PROCESSOR HALT (160000)	
HSYNC	HORIZONTAL SYNC (VIDEO)	
INDEX	INDEX PULSE (FLOPPY)	
INT(2,3,6)	INTERRUPT REQUEST (CHIPS)	
IORESET	I/O RESET	
INTL(2:0)	INTERVIEW PRIORITY LEVEL (160000)	
KBCLK	KEYBOARD CLOCK (KEYBOARD)	
KBDATA	KEYBOARD DATA (KEYBOARD)	
KBRESET	KEYBOARD RESET (KEYBOARD)	
LD5/UD5	UPPER / LOWER DATA STROBES (160000)	
LED	POWER ON LED / AUDIO FILTER DISABLE	
LEFT/RIGHT	LEFT RIGHT AUDIO (AUDIO)	

SIGNAL	DESCRIPTION (AREA)	PAGES
LEPN	LIGHT PEN TRIGGER (JOYSTICKS)	
MTR	MOTOR ON (FLOPPY)	
MRO	MOTOR ON - DRIVE 0 (FLOPPY)	
MV/MOM	MOUSE 0 QUADRATURE V/M (JOYSTICKS)	
MV/MIM	MOUSE 1 QUADRATURE V/M (JOYSTICKS)	
QV	OVERLAY REGISTER RST	
QVR	OVERLAY SYSTEM DECOUING	
PIXELSW	GENLOCK PIXEL SWITCH (VIDEO)	
PCTX0/0V	POT LINES 0 X/Y (JOYSTICKS)	
PCTX1/Y	POT LINES 1 X/Y (JOYSTICKS)	
PDI0	PAPER OUT (PARALLEL PORT)	
PDI0(2,0)	PARALLEL PORT DATA (PARALLEL PORT)	
PMEN	RAM ENABLE (CHIPS)	
PMEN	CHIP REGISTER ENABLE (CHIPS)	
PS0/0	ROW ADDRESS STROBE (DRAM)	
RIV	DRIVE READY (FLOPPY)	
RESET	GENERAL RESET	
RS0A(1)	REGISTER ADDRESS BUS (CHIPS)	
R/G/B	RED GREEN BLUE (VIDEO)	
R1	RING INDICATE (RS232 PORT)	
R1MEN	ROM ENABLE (ROM)	
R1S	REQUEST TO SEND (RS232 PORT)	
RST	PROCESSOR RESET (SCAND)	
RXD	RECEIVE DATA (RS232 PORT)	
R1	PROCESSOR READY (SCAND)	
SEL	SELECT (PARALLEL PORT)	
SEL(3,0)	DRIVE SELECT (FLOPPY)	
SIDE	SIDE SELECT (FLOPPY)	
STEP	STEP IN/OUT COMMAND (FLOPPY)	
TRKD	TRACK ZERO SENSE (FLOPPY)	
TXD	TRANSMIT DATA (RS232 PORT)	
VAD	VALID MEMORY ADDRESS (68000)	
VPA	VALID PERIPHERAL ADDRESS (68000)	
VSYNC	VERTICAL SYNC (VIDEO)	
WE	WRITE ENABLE (DRAM)	
WEPROT	WRITE PROTECT SENSE (FLOPPY)	
XCLK	EXTERNAL CLOCK SIGNAL (VIDEO)	
XCLKEN	EXTERNAL CLOCK ENABLE (VIDEO)	
XRDY	EXTERNAL DATA READY	
== CREDIT CARD AND IDE STUFF? ==		

KEY COMPONENTS

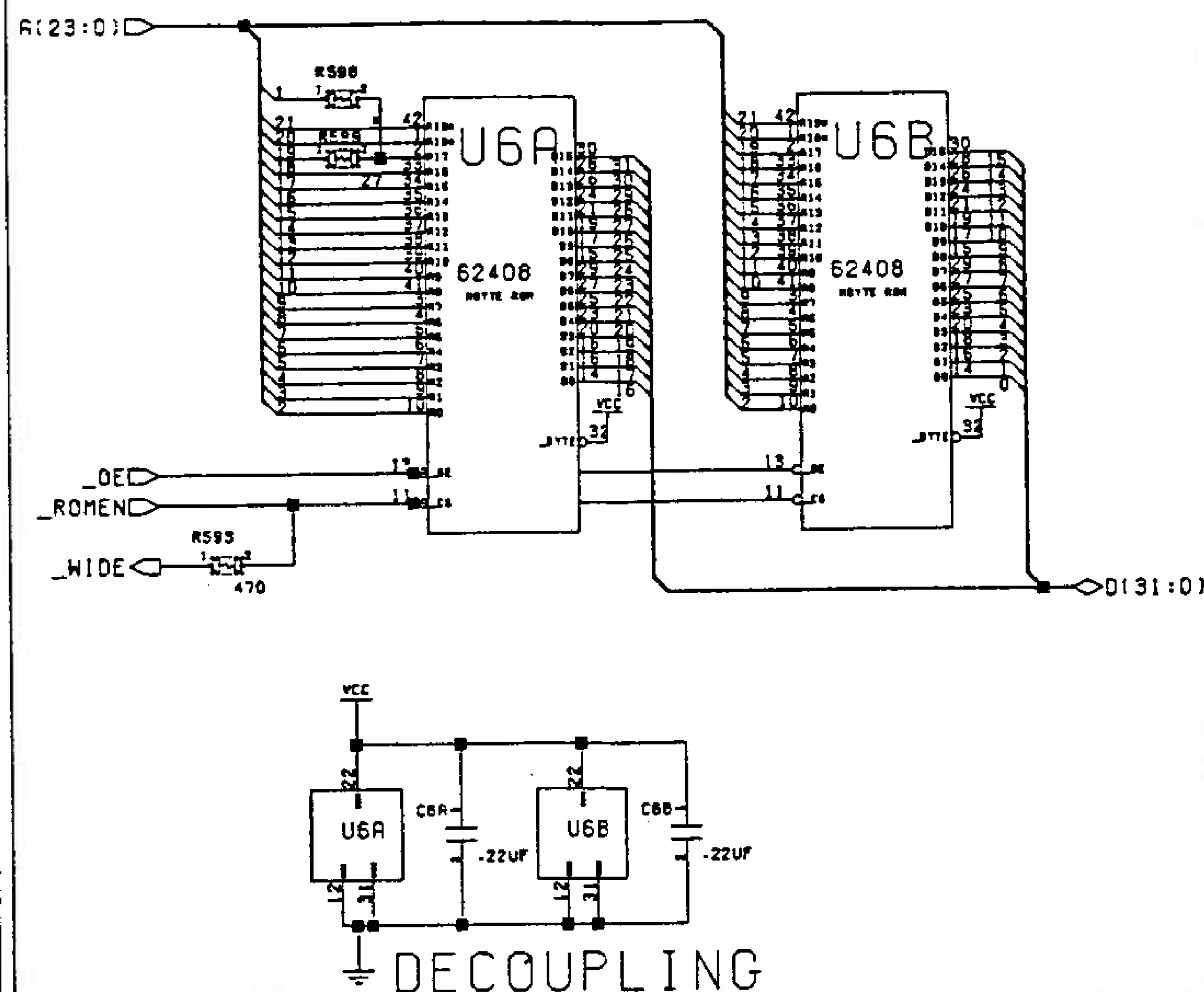
REF	CHIP	DESCRIPTION	PAGE
U1	68000	68000 PROCESSOR 16MHZ	2
U2	18374	ASIC (AA DENISE)	2
U3	18364	PAULA	5
U4	4203	LSA (AA DENISE)	4
U5	7202A	AA GAYLE (CBA ASIC)	2, 8, 1
U6	8551	ROM 512KX16, 10% NS	10
U7-B	B520	RAM 64 K 1 MHz	7
U10-1	28A10	FLASH MEMORY 128KX8	10
U12	7414-15	CMT 14-15	4
U13	56H005	RAM 16 K 1 MHz	4
U14	PS1510	LOW VOLTAGE SENSE IC	19
U15	LF947	BIOS OF-AMP	15
	11 GP4	BIOS OF-AMP	15
U16-17	8551	RAM 256KX16, 60% NS	3
U18-19	8551	RAM 256KX16 OPTIONAL	3
U20	351777	BUGGIE (ASIC)	2
U28	11448	EIA LINE DRIVER	7
U29	1449	EIA LINE RECEIVER	7
U30	AT101	TRIPLE 8-BIT VIDEO CARD	4
X1	OSC	11L 28.63636 MHZ N1SC 12	
	OSC	11L 28.37512 MHZ PAL 12	
Y451	XIAL	4.35169MHZ PAL BURST 14	
Y621	XIAL	3MHZ CROMAT RESONATOR 1059	
X2	8551	PAL VIDEO MODULATOR 14	
	8551	N1SC VIDEO MODULATOR 14	

16-BIT ROM



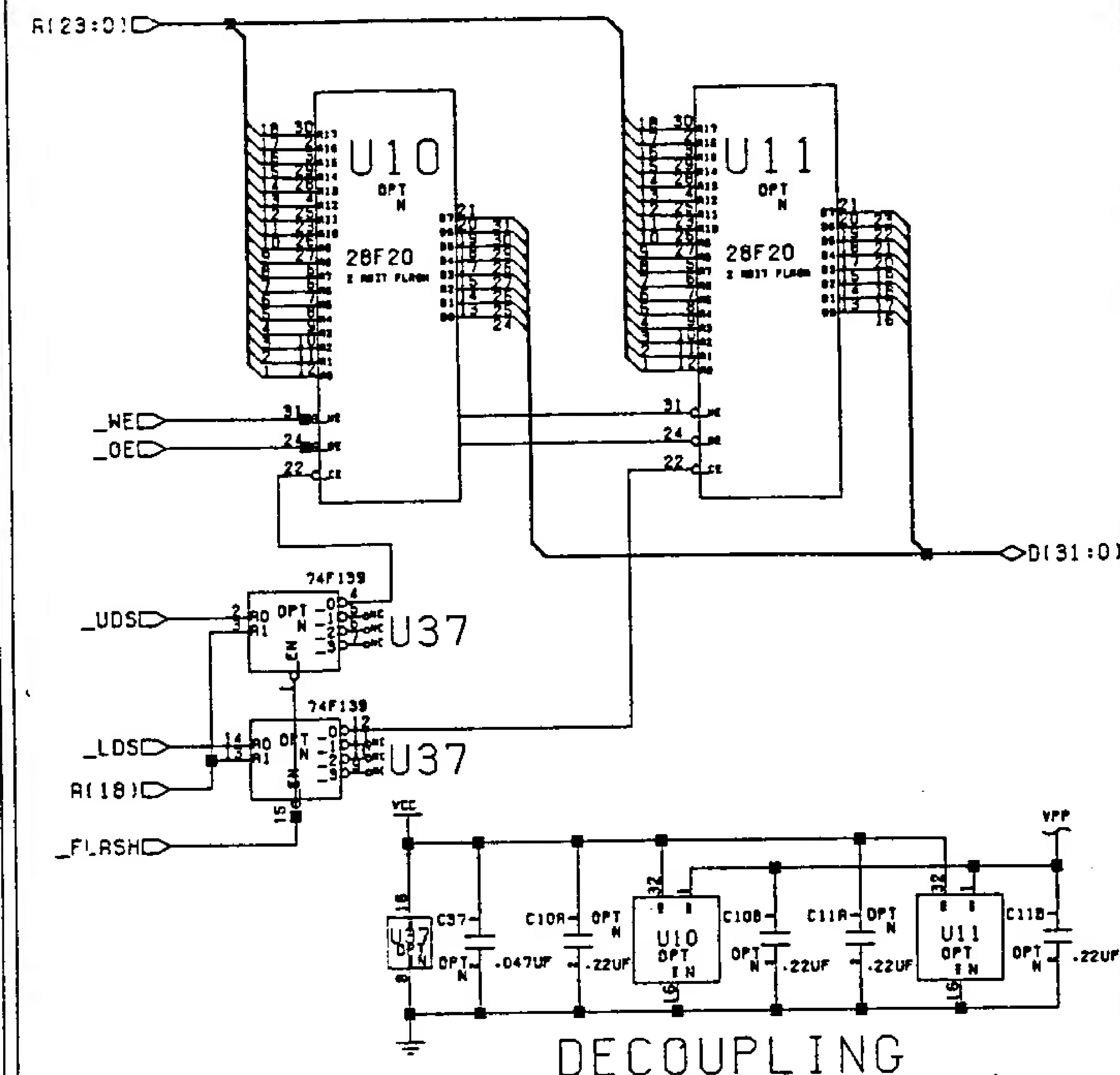
IF ROM16="YES"

32-BIT ROM



IF ROM32="YES"

FLASH MEMORY

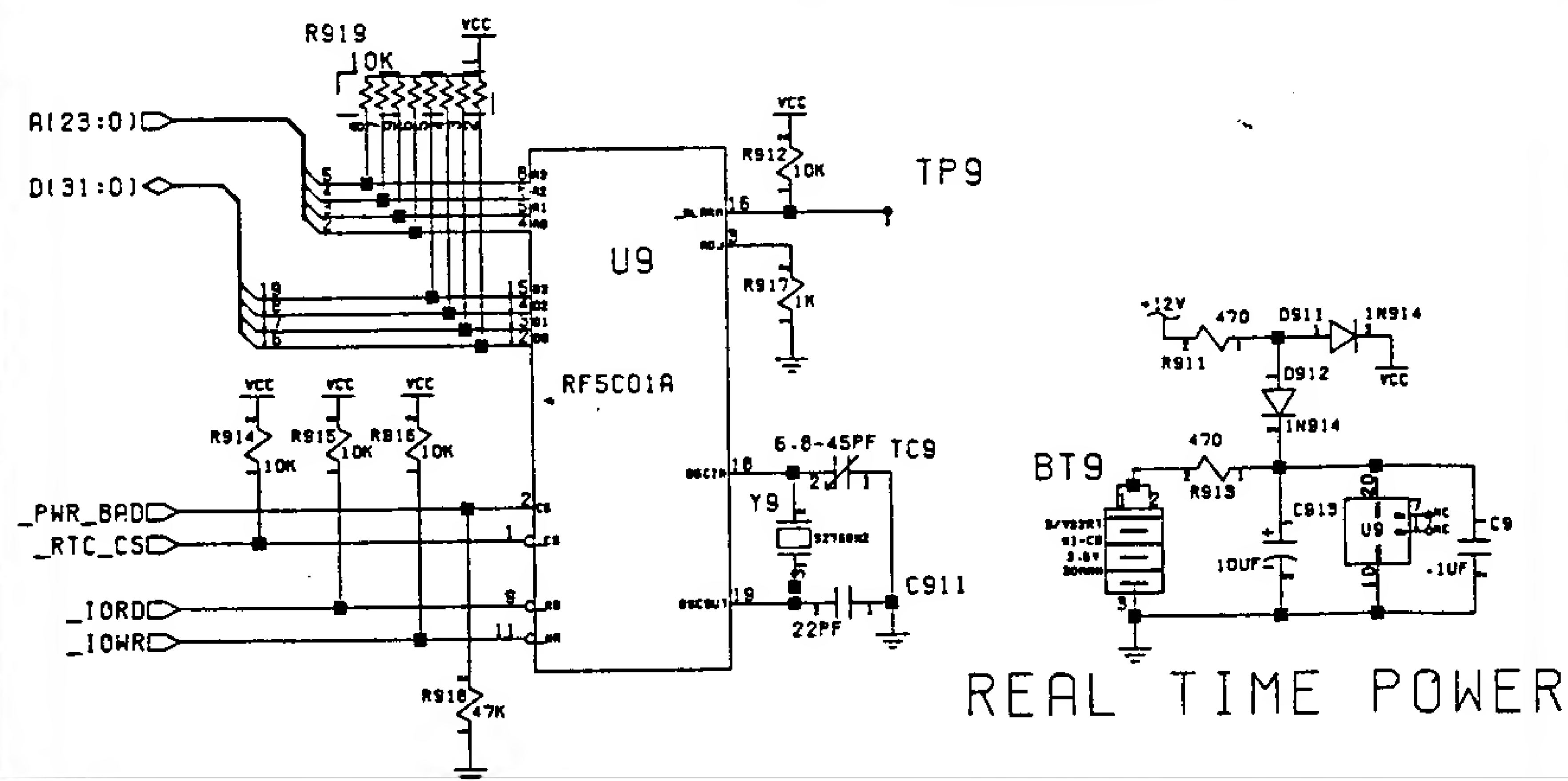


IF FLASH="YES"

16 AND 32-BIT SOCKETS MAY OVERLAP!

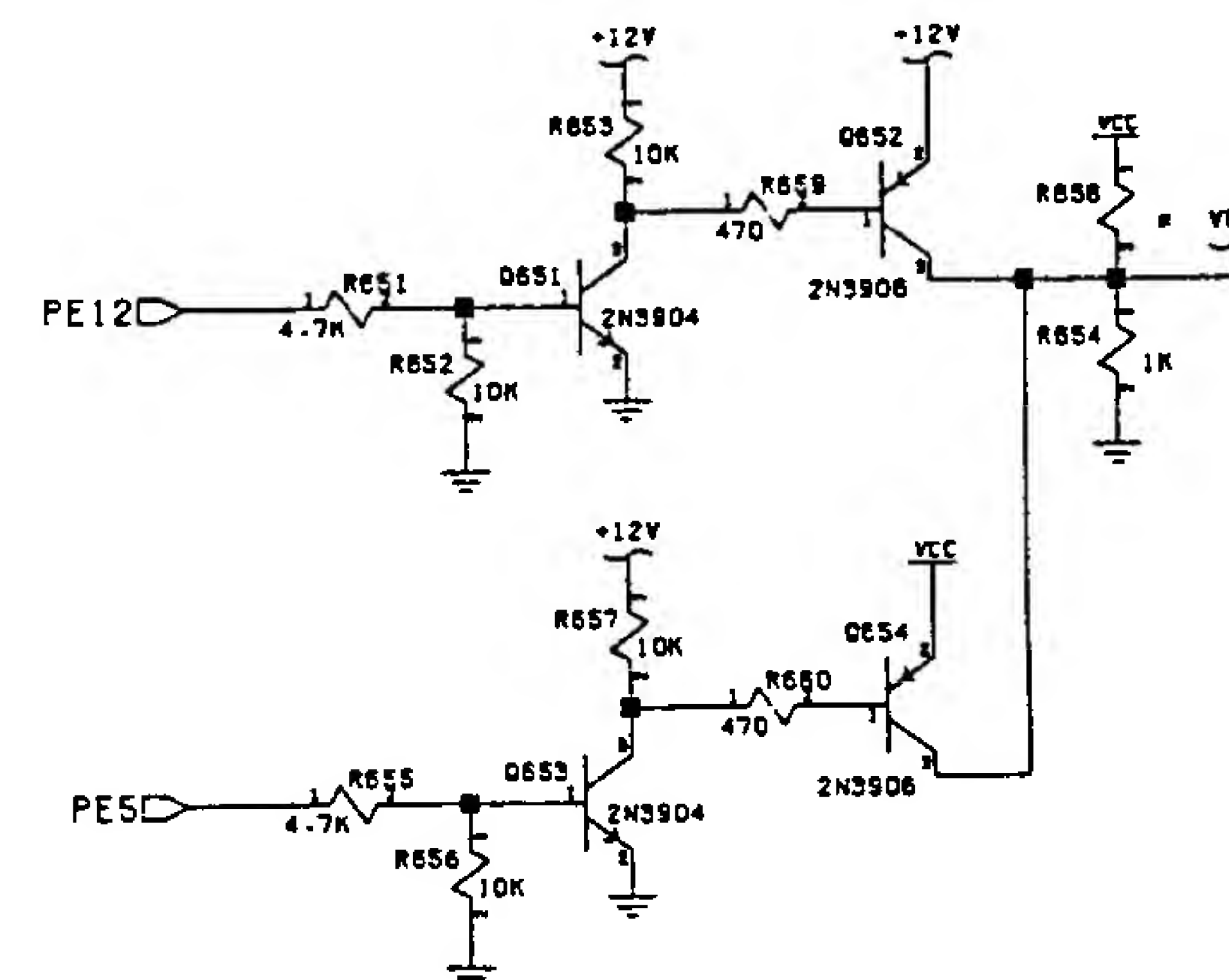
OPTIONAL FLASH MEMORY

REAL TIME CLOCK



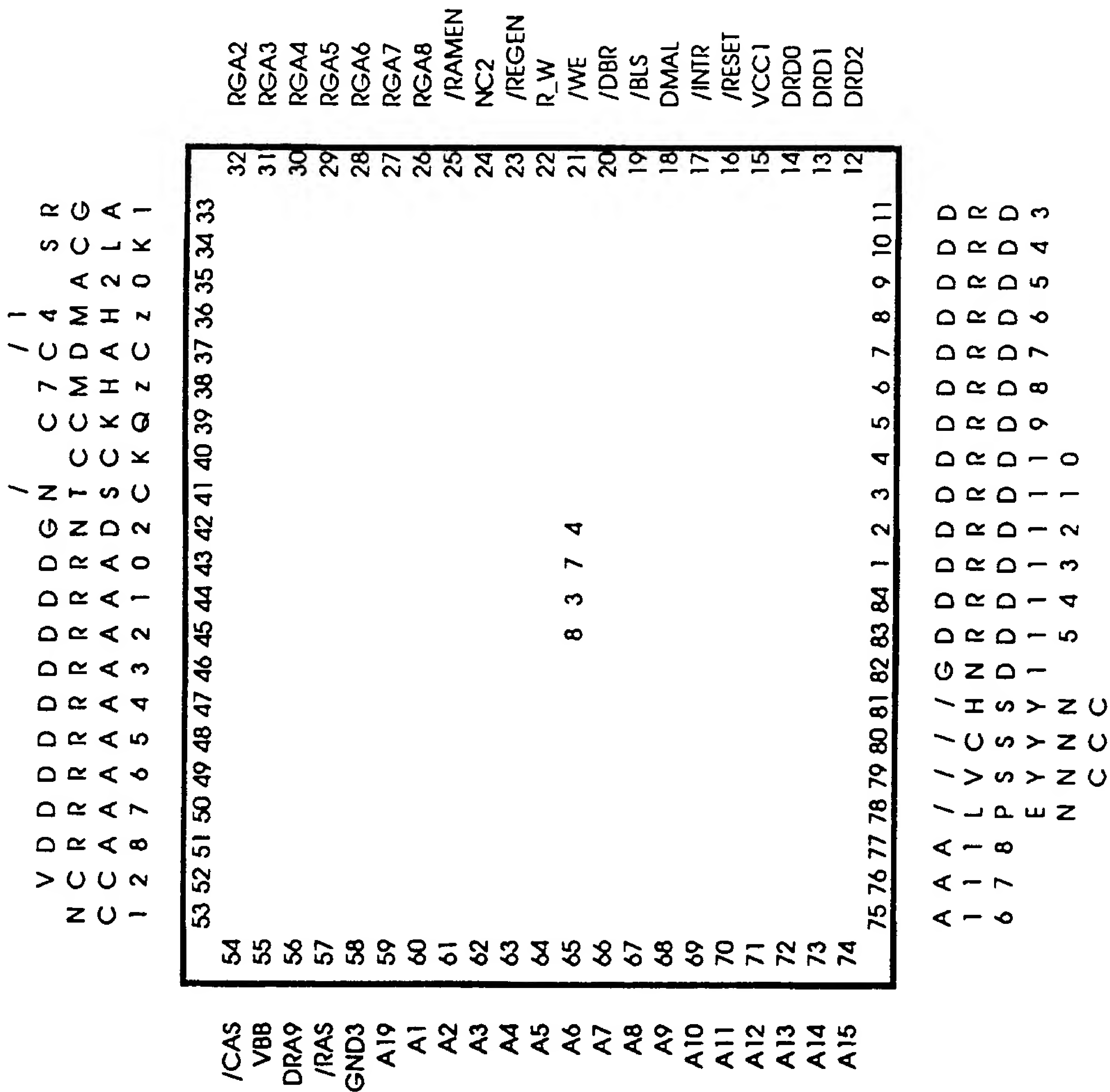
IF RTC="YES"

PROGRAMMING VOLTAGE



OPTIONAL REAL-TIME CLOCK/CALENDAR

A1200 REV 1->1D PCB



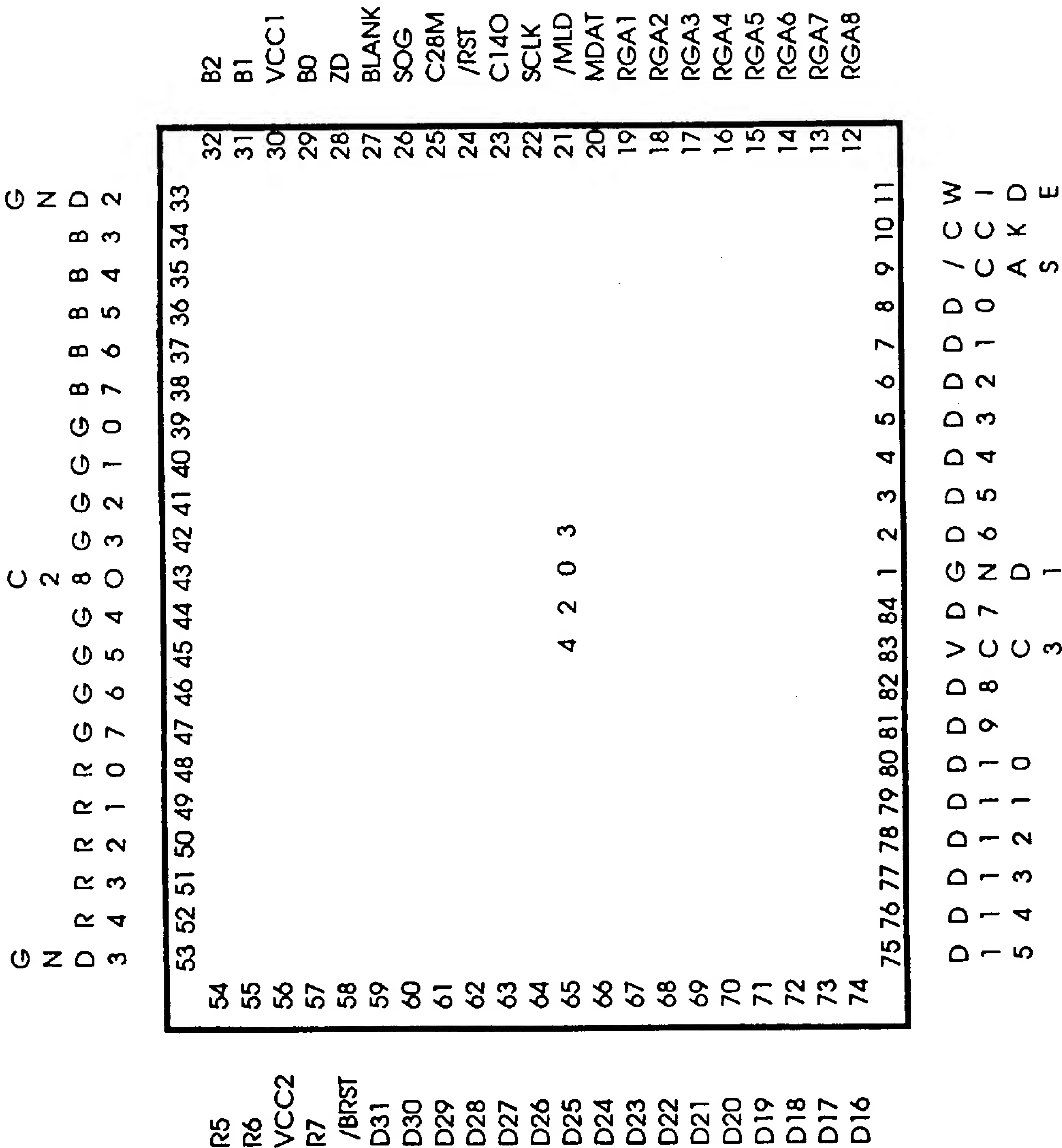


Figure 5-6. LISA

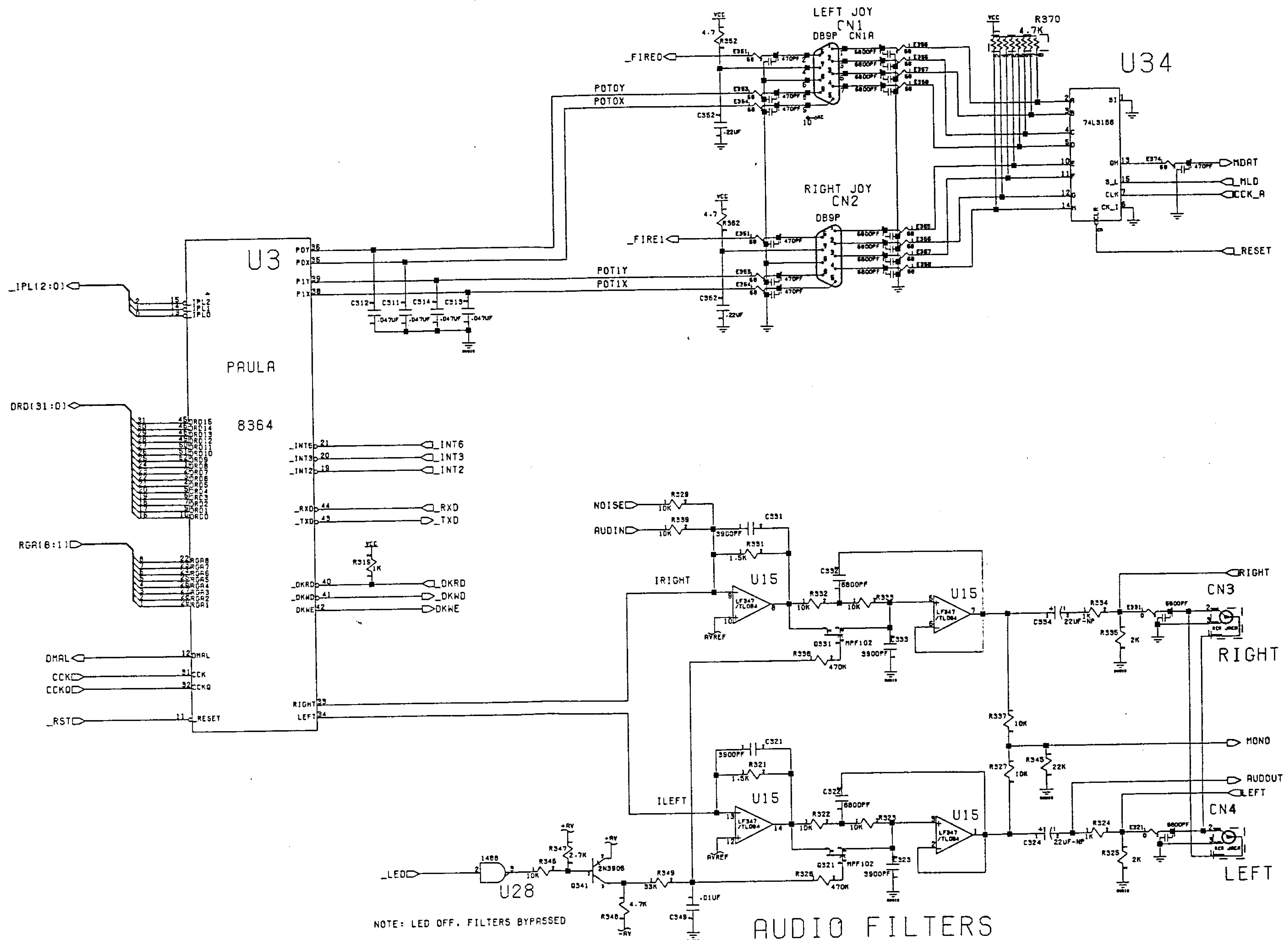
D(31:0) 

A(23:0) 

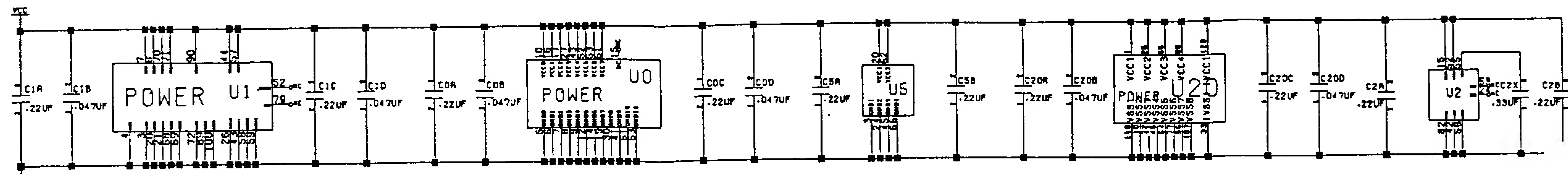
FC(2:0) 



MOUSE/JOYSTICK PORTS

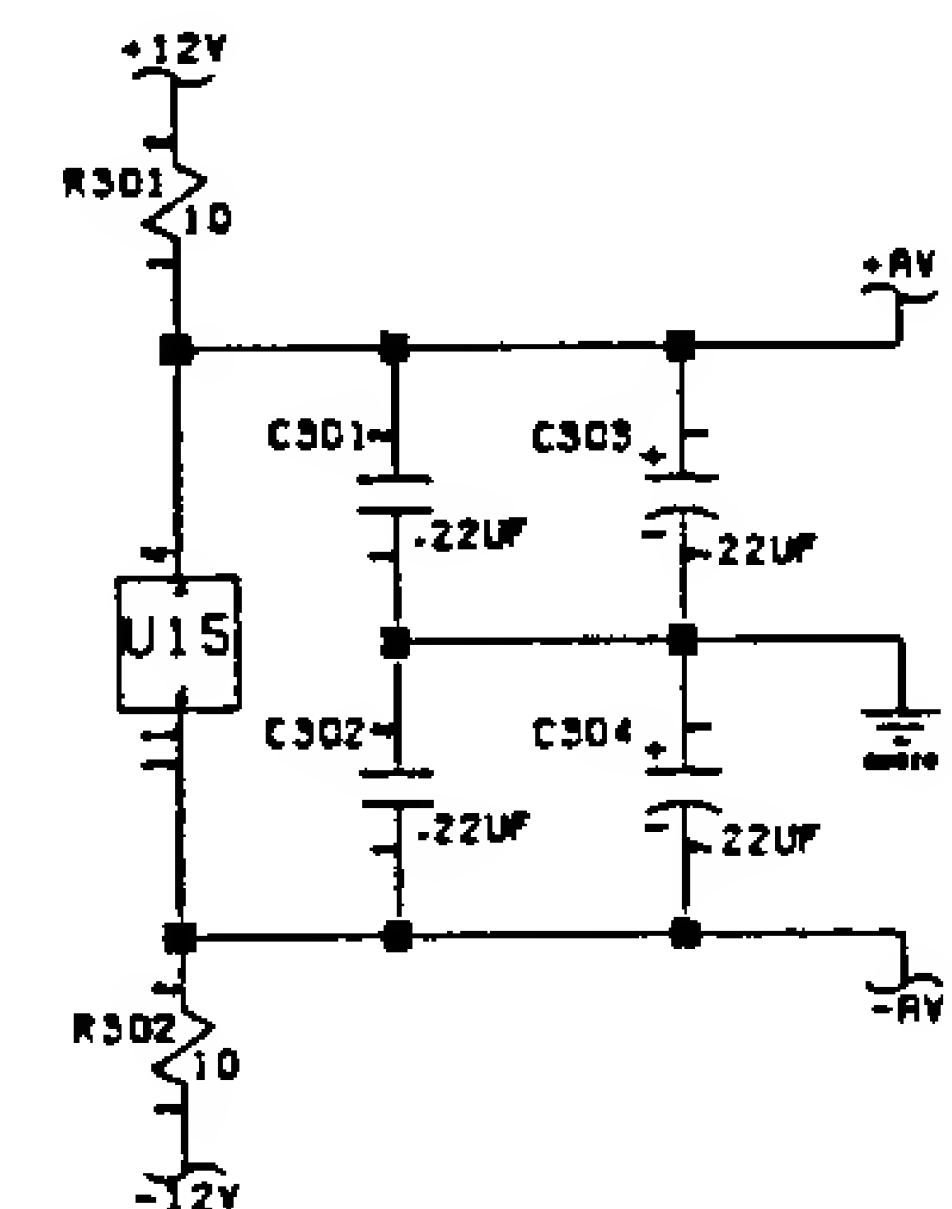
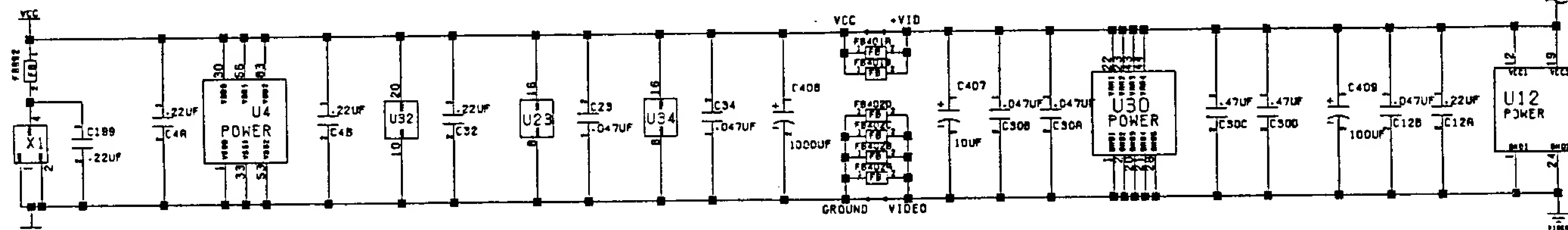


GENERAL DECOUPLING



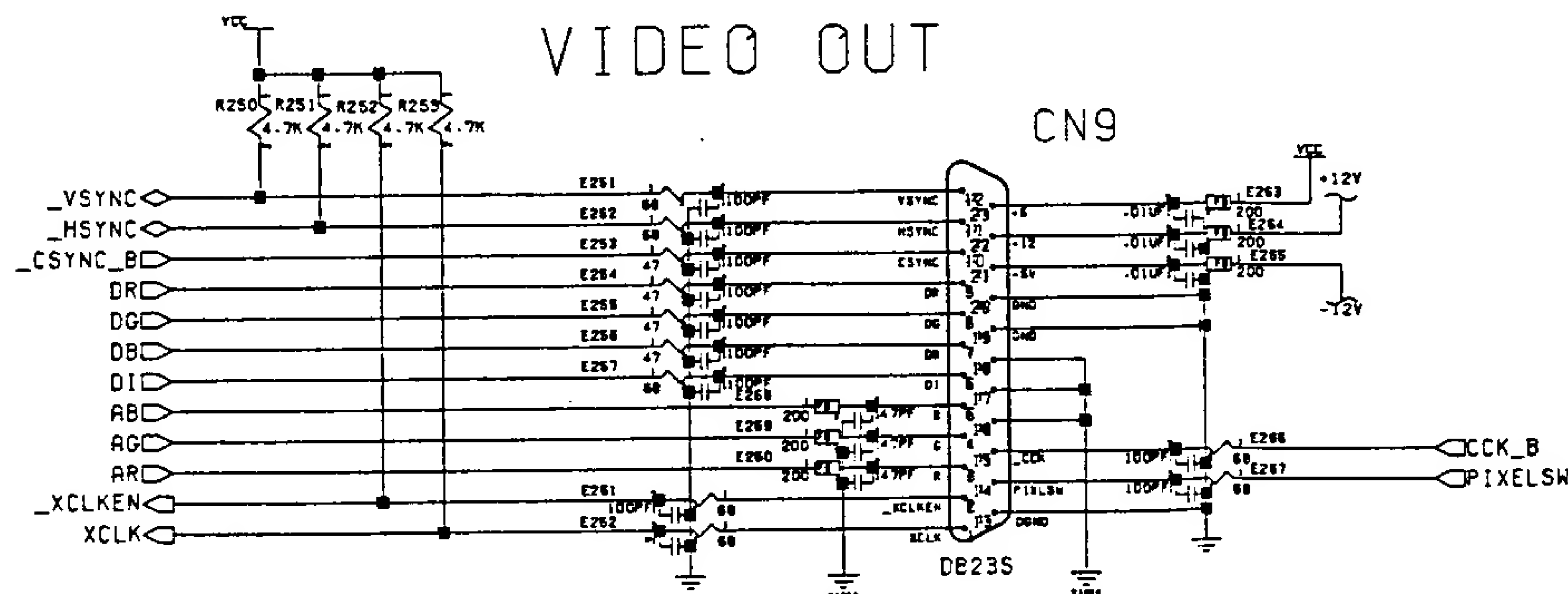
VIDEO DECOUPLING

AUDIO DECOUPLING

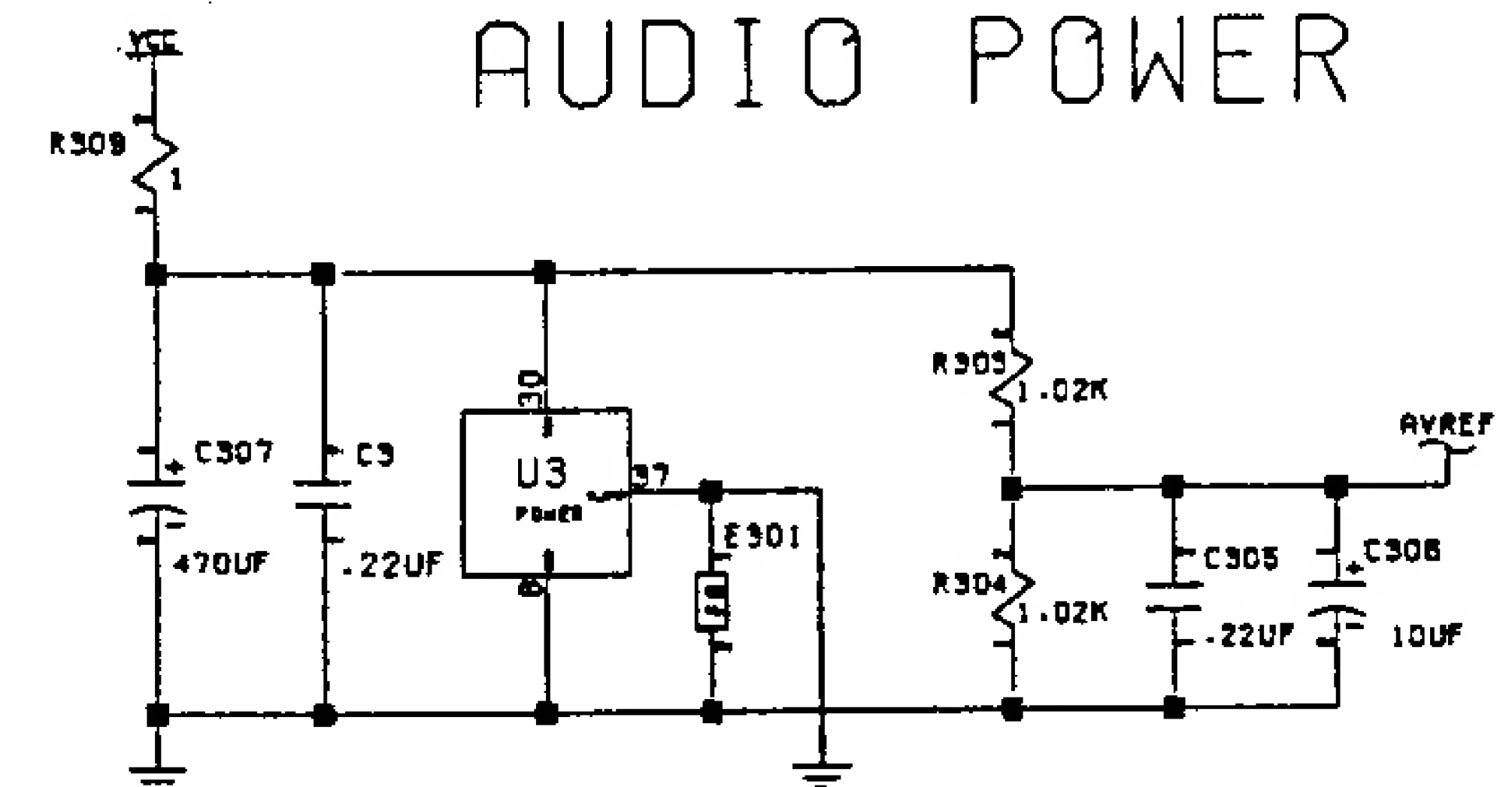


NOTE: AS OF REV 1C, LOGIC AND VIDEO GROUND AND POWER ARE THE SAME NET, BUT ROUTED DISCRETELY EXCEPT AT DAC1
ALSO ADDED C30C AND C30D FOR OVERKILL DAC DECOUPLING.

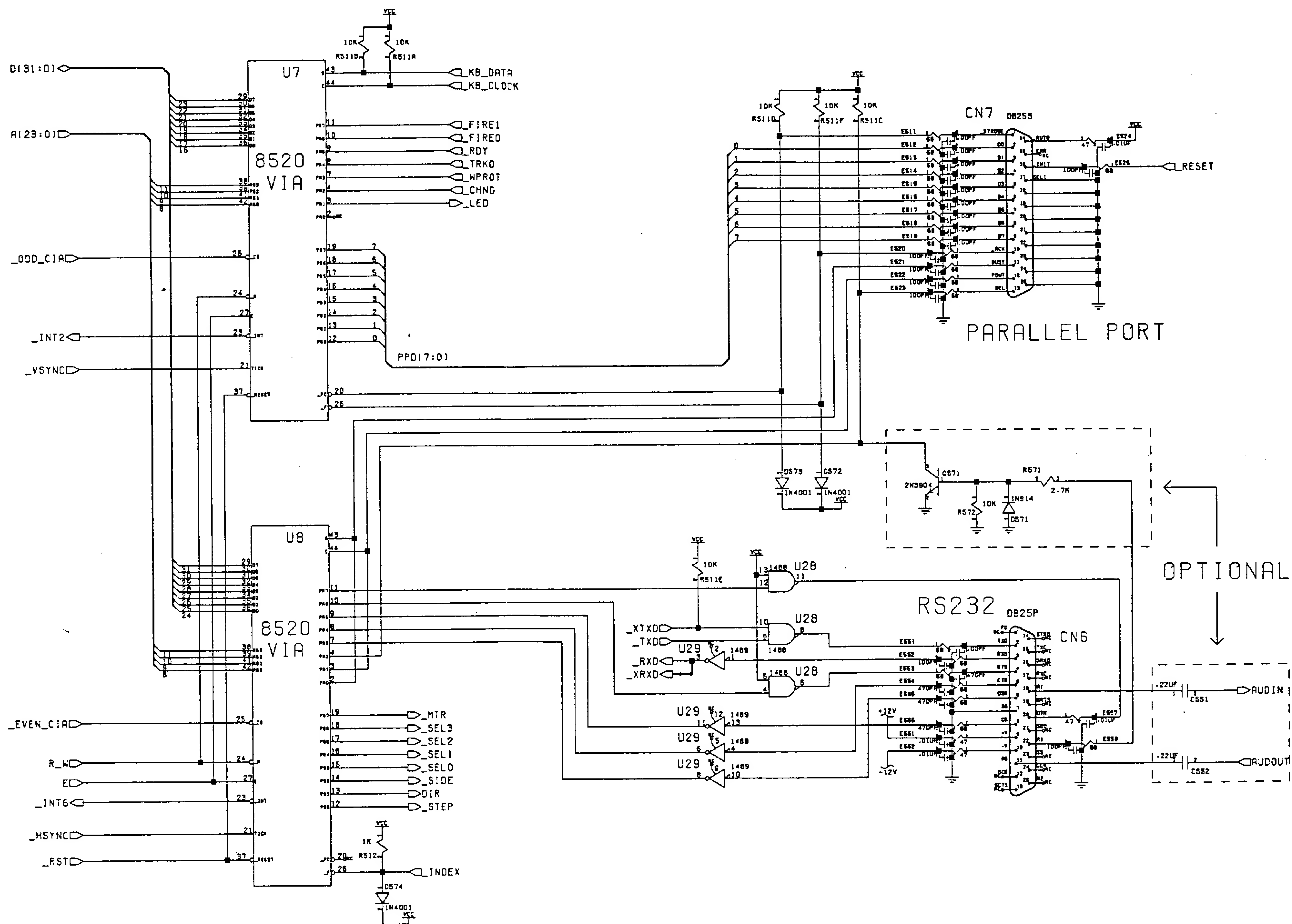
VIDEO OUT



AUDIO POWER

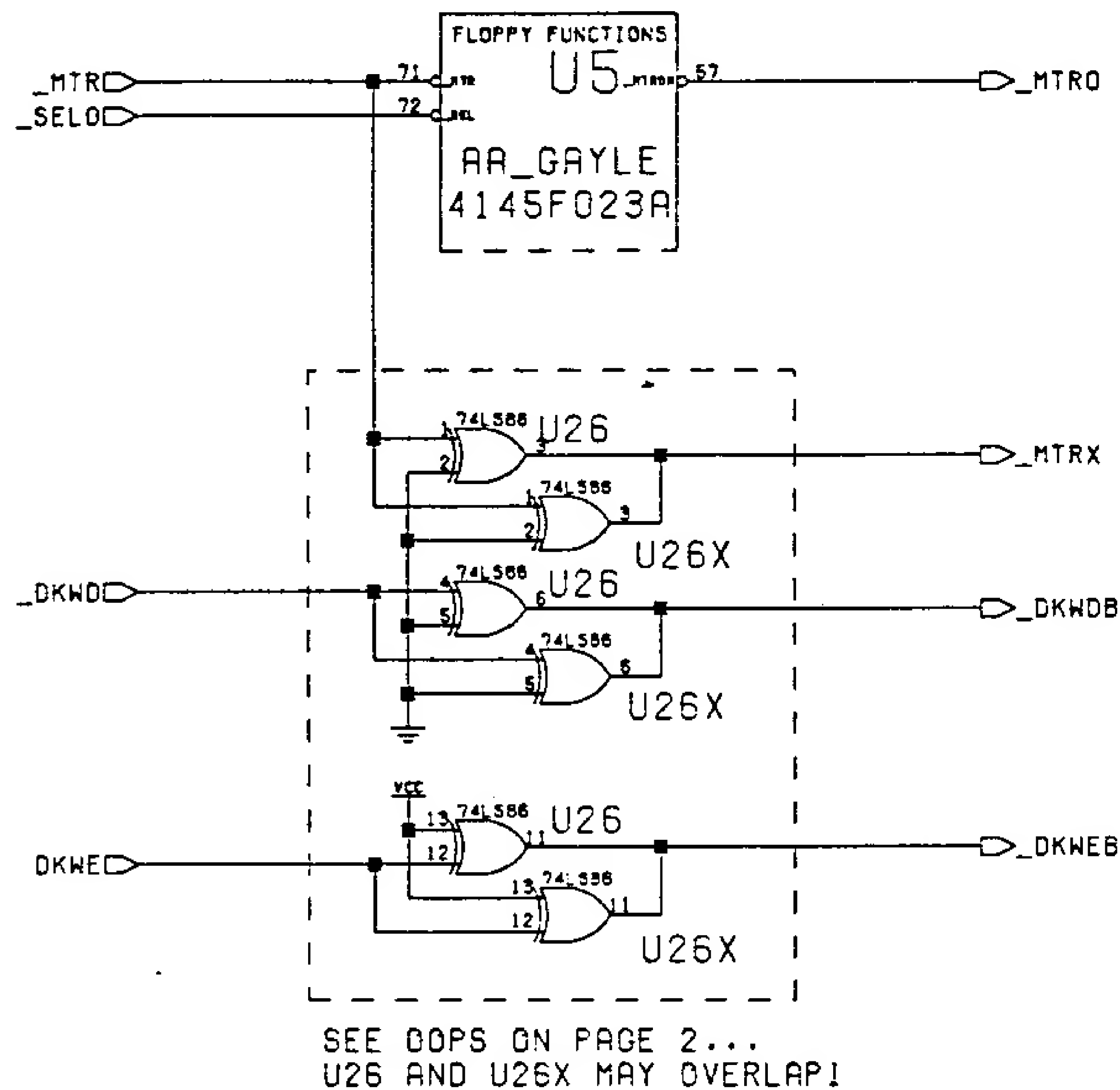


NOTE: GROUND INTERCONNECTION NEAR AUDIO JACKS.

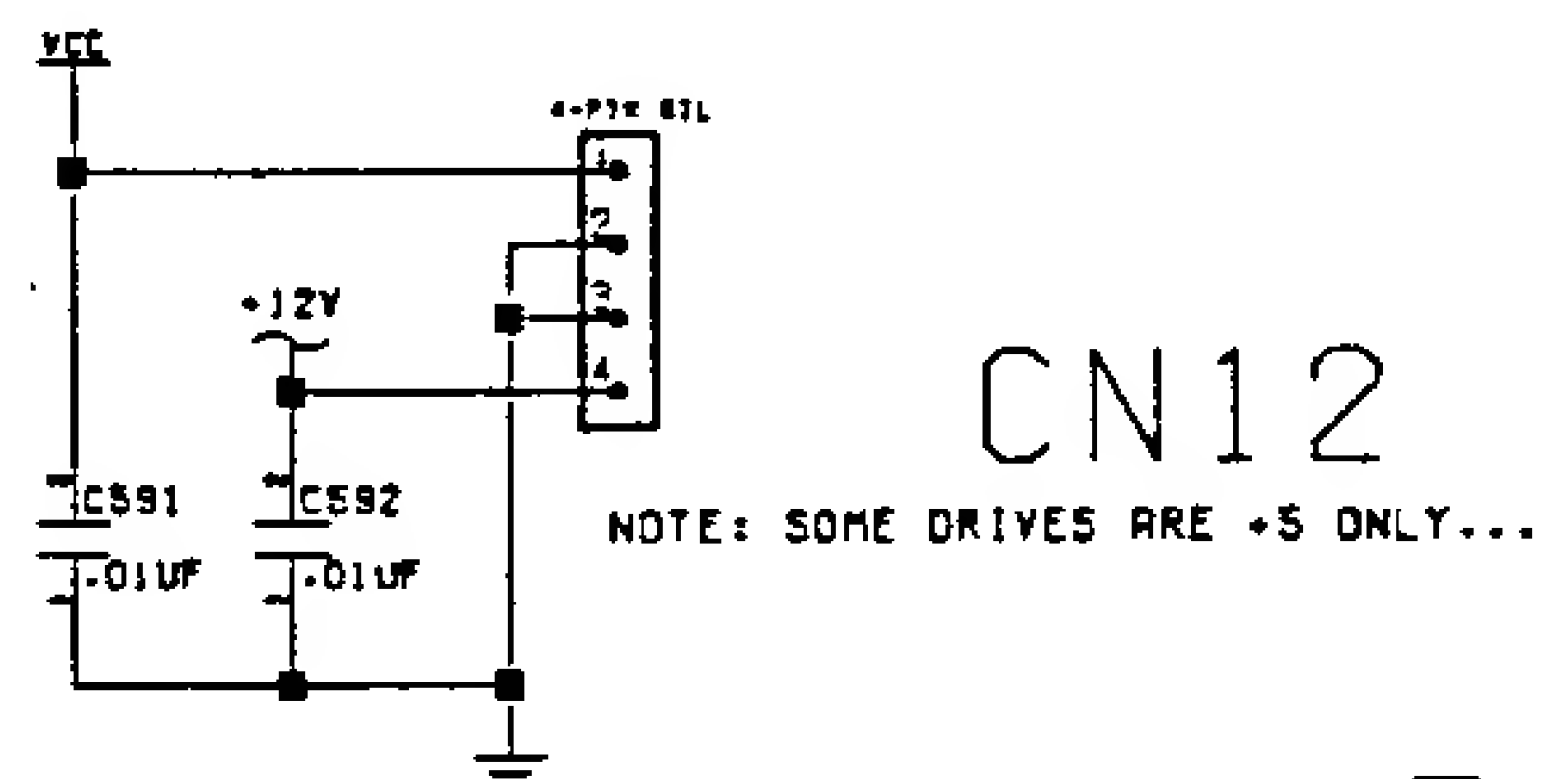


A1200 REV 1->10 PCB

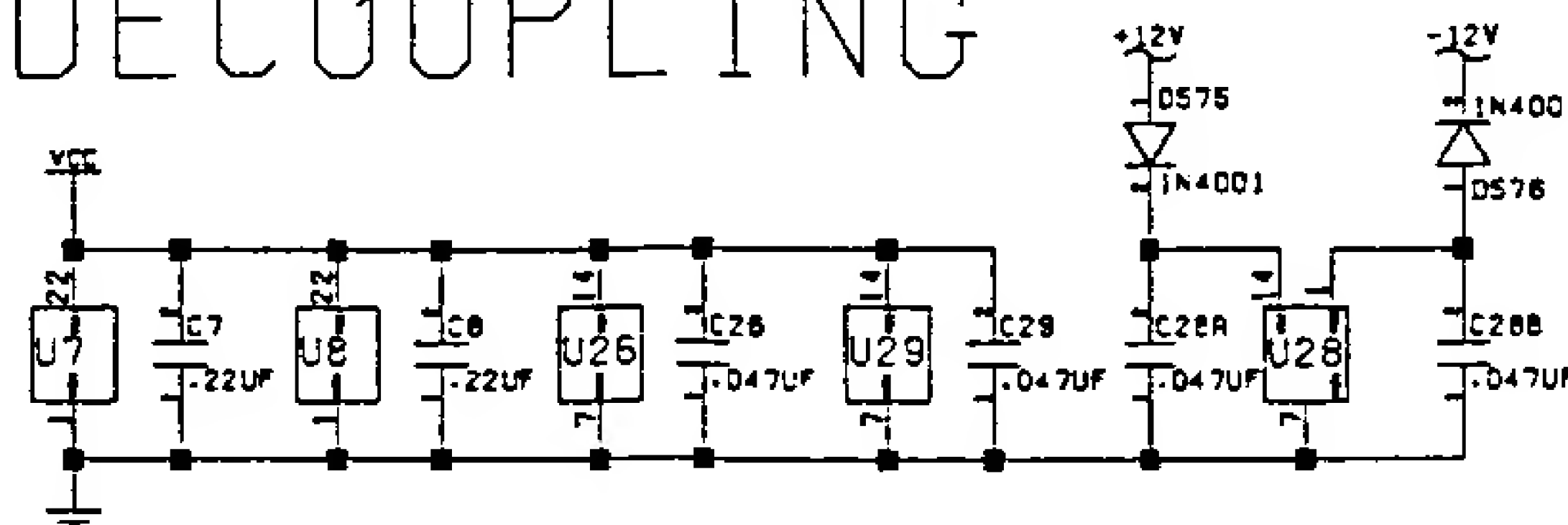
FLOPPY LOGIC



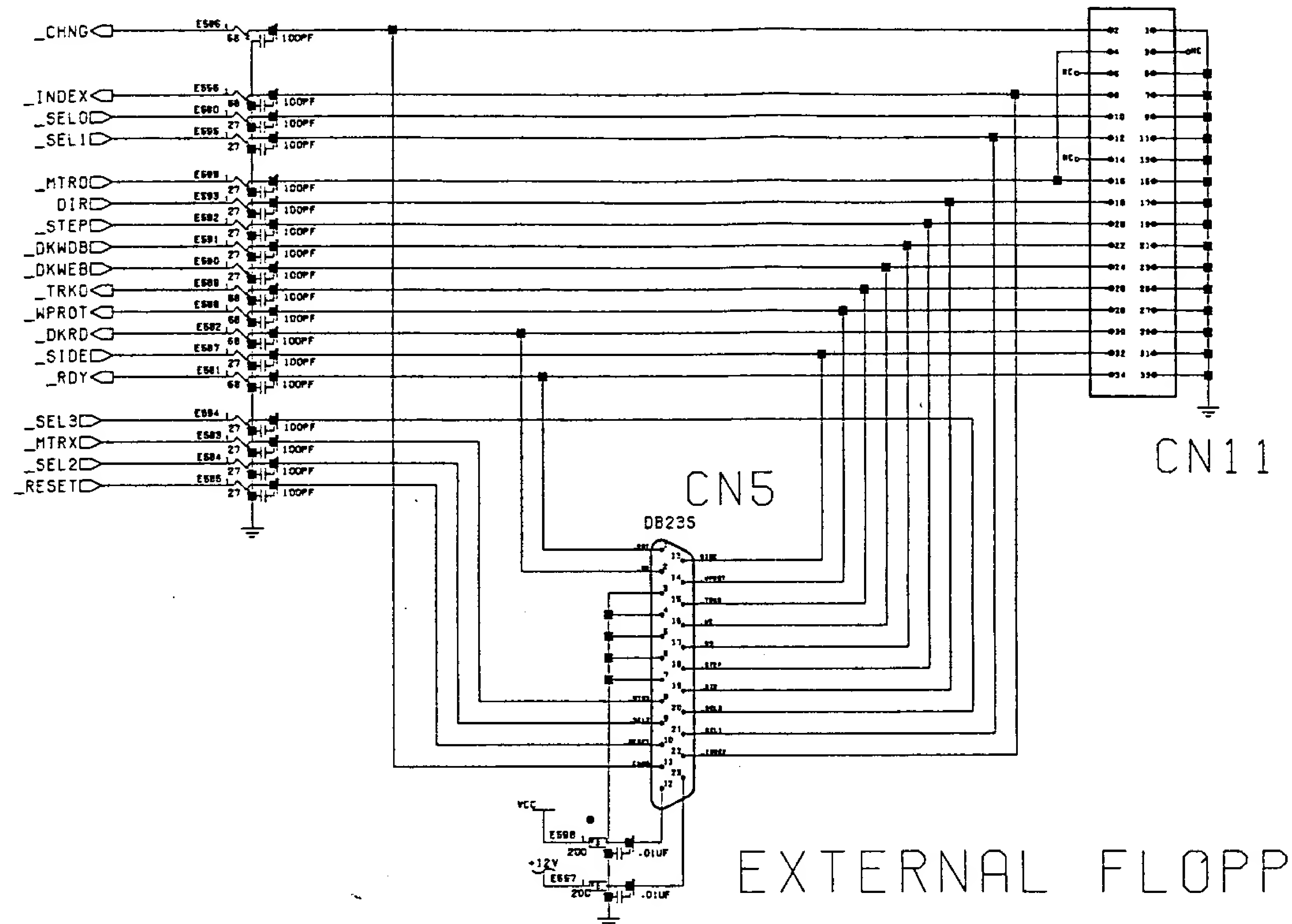
FLOPPY POWER



DECOUPLING



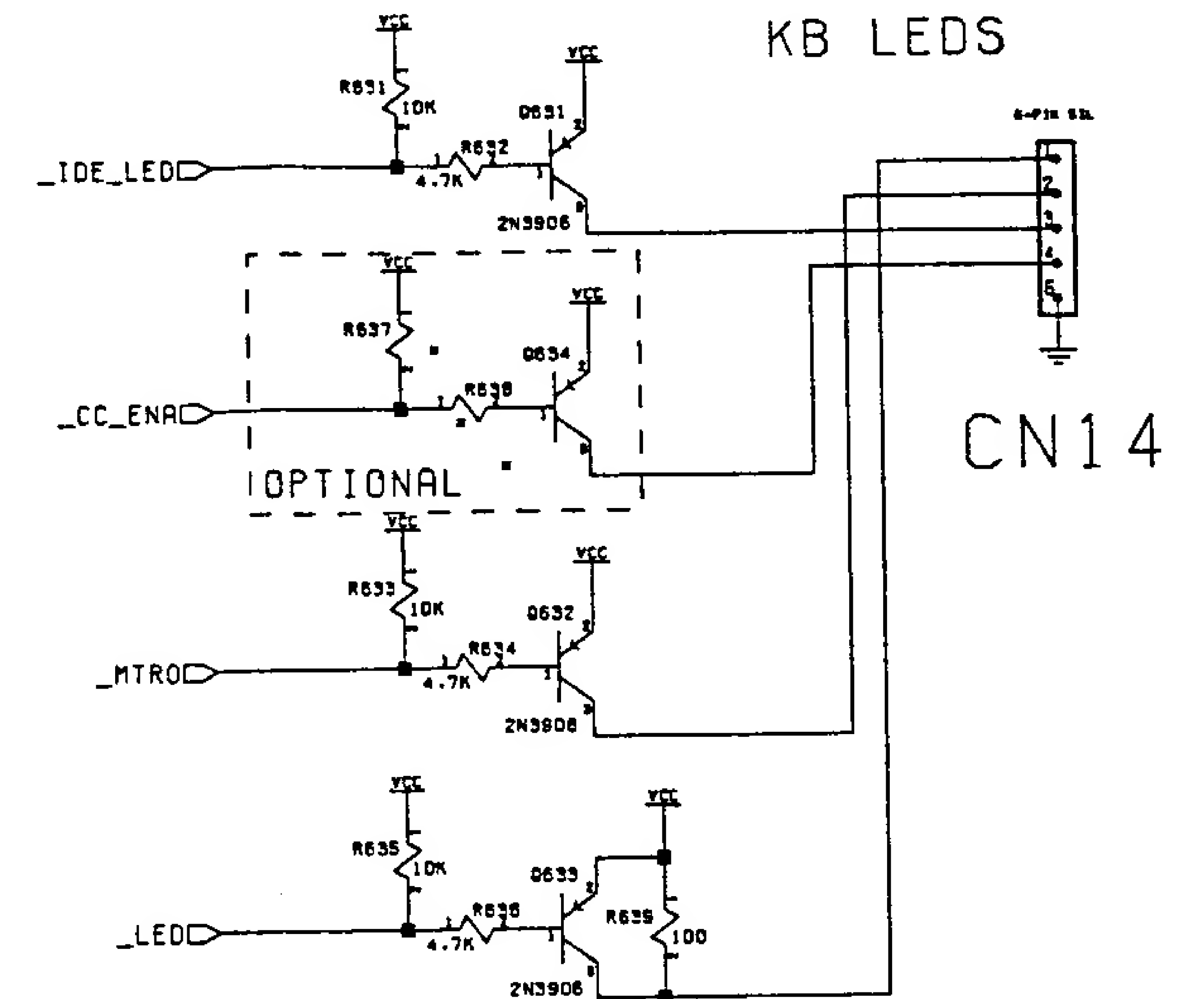
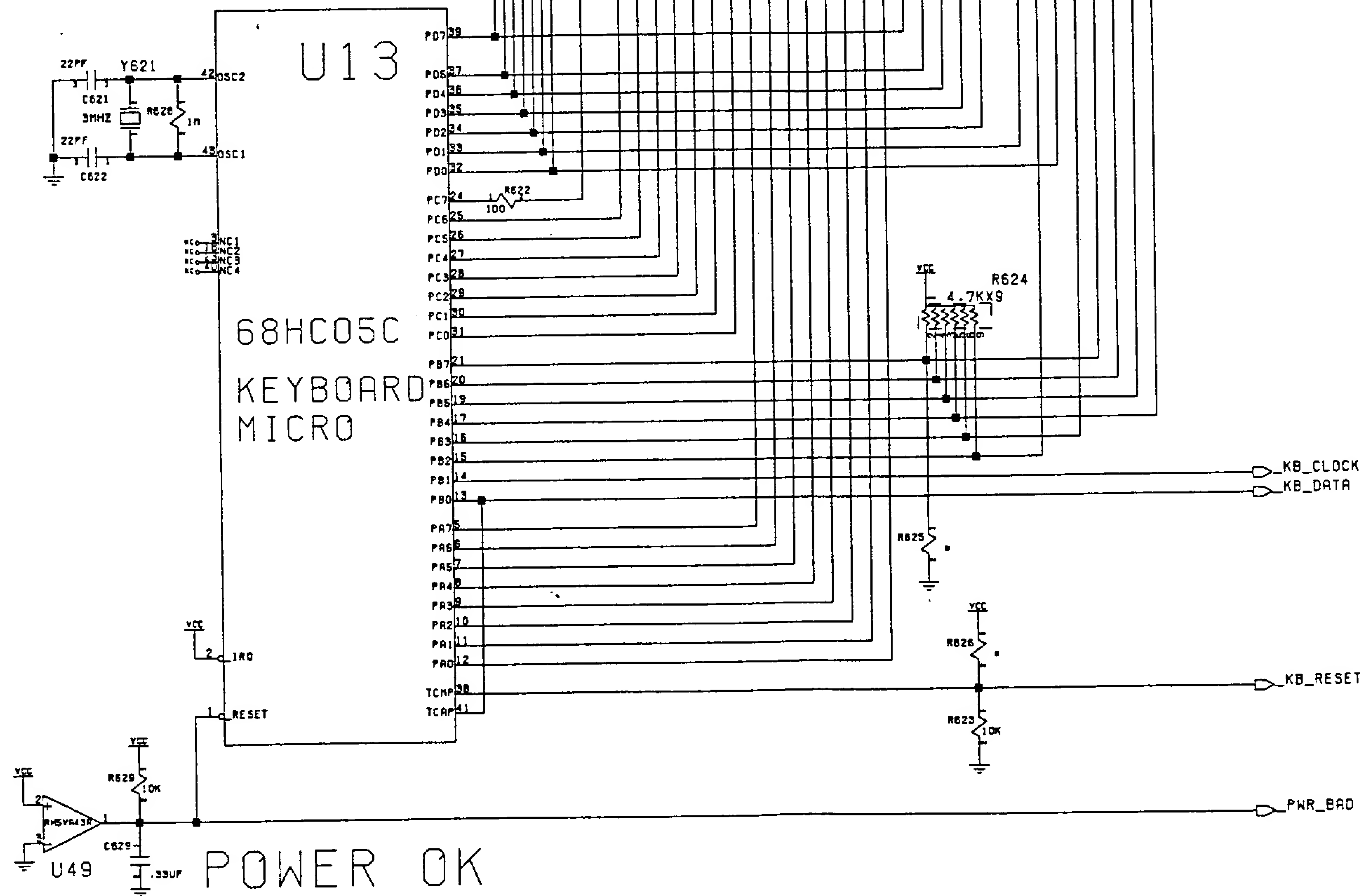
INTERNAL FLOPPY



EXTERNAL FLOPPY

KEYBOARD MPU

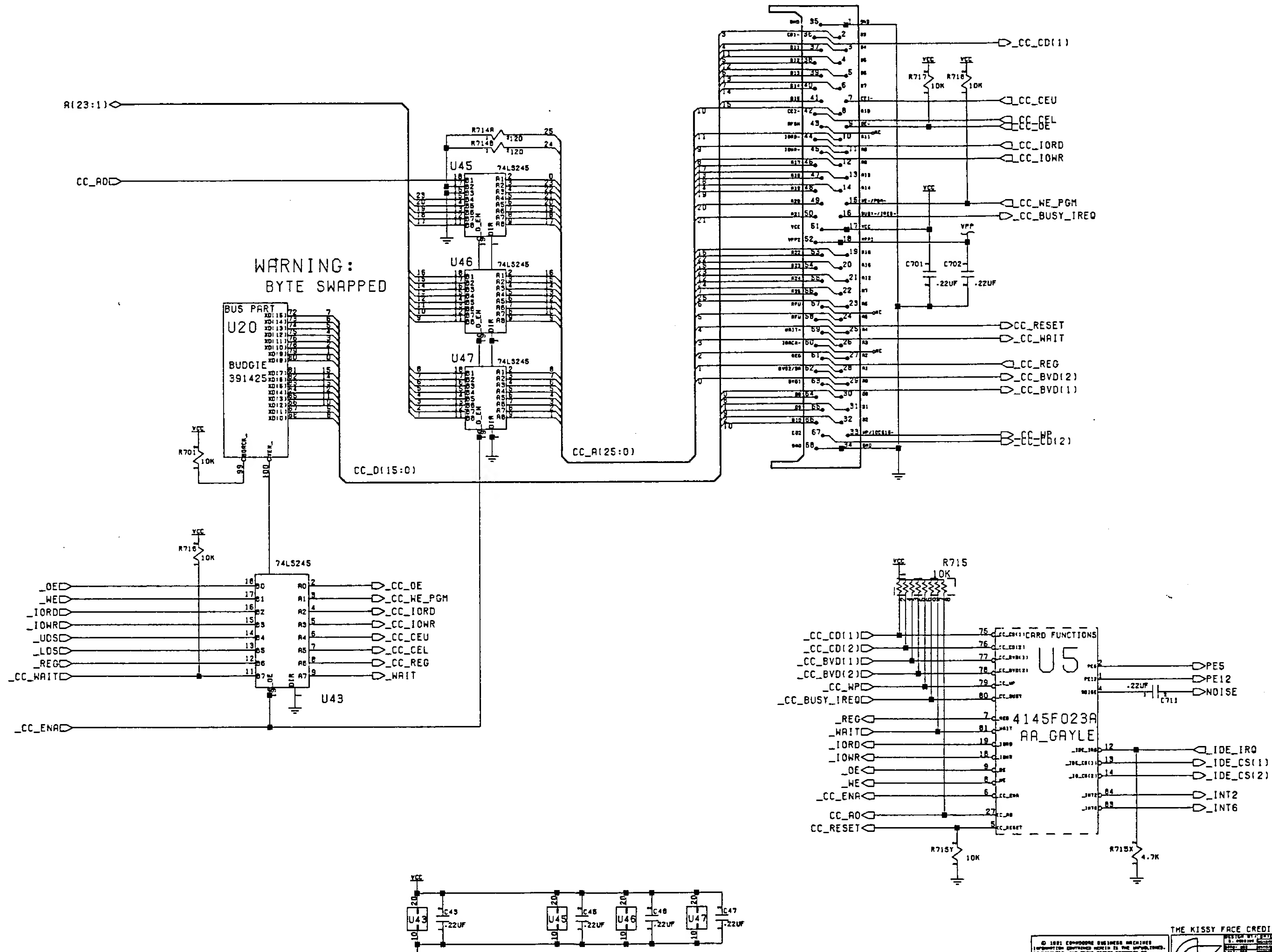
KEYBOARD TAIL CN13



A1200 REV 1->1D PCB

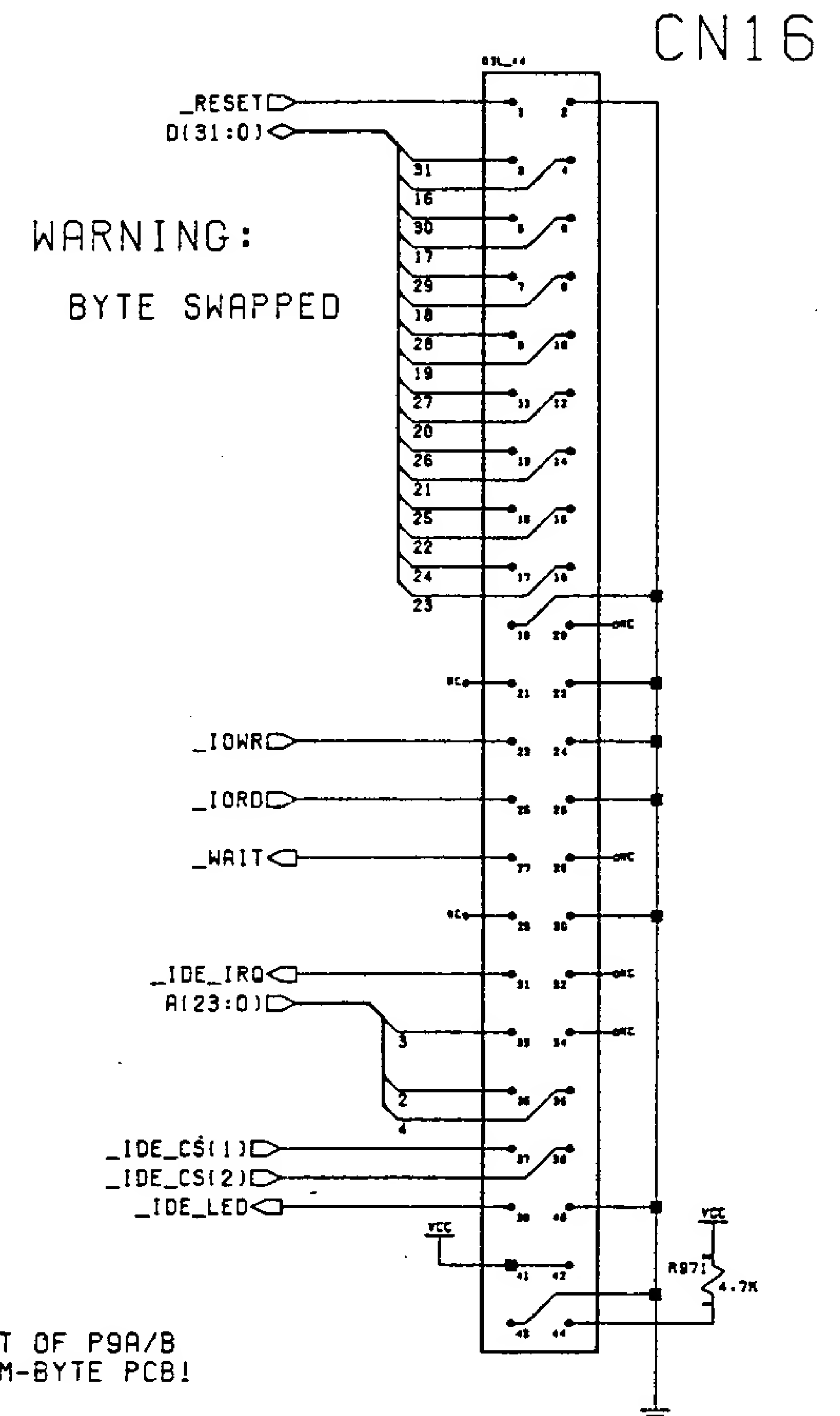
MEMORY CARD

CN15



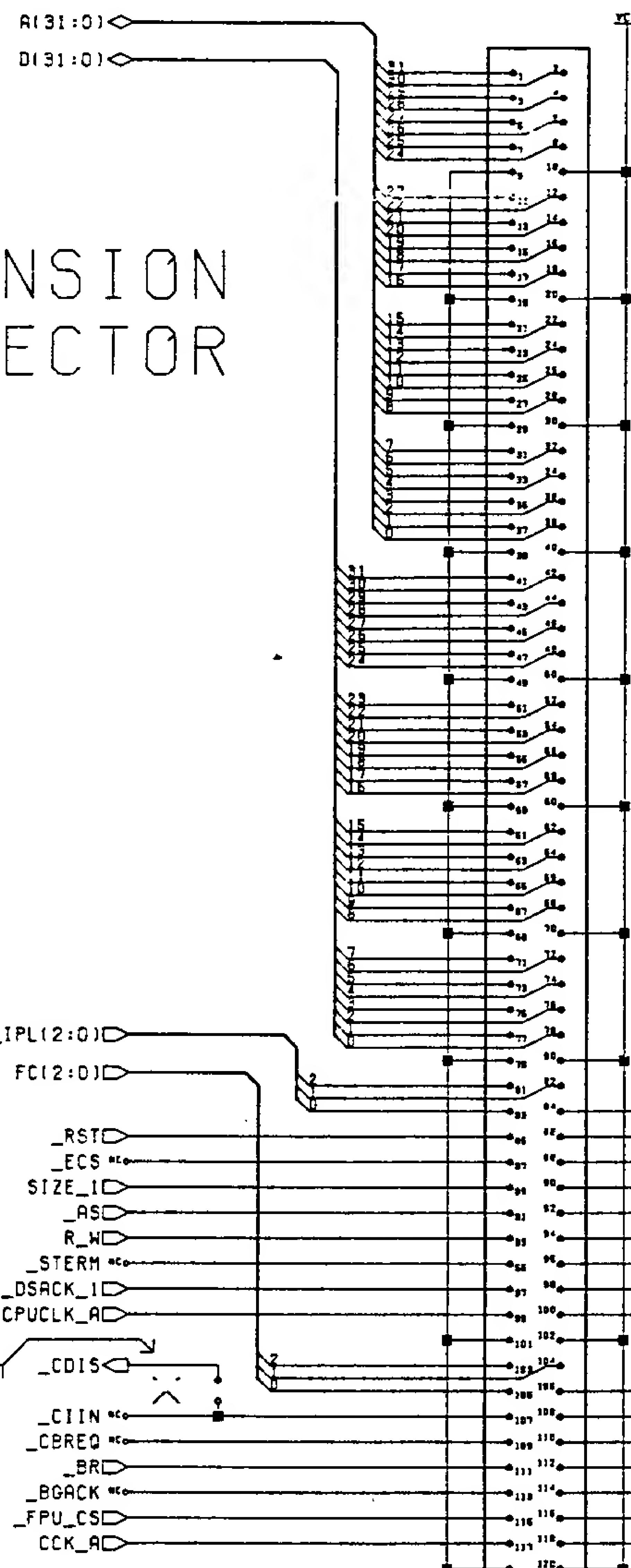
A1200 REV 1->1D PCB

IDE DRIVE



NOTE: ONLY THIS BIT OF P9A/B
LOADED FOR 2M-BYTE PCB!

EXPANSION CONNECTOR

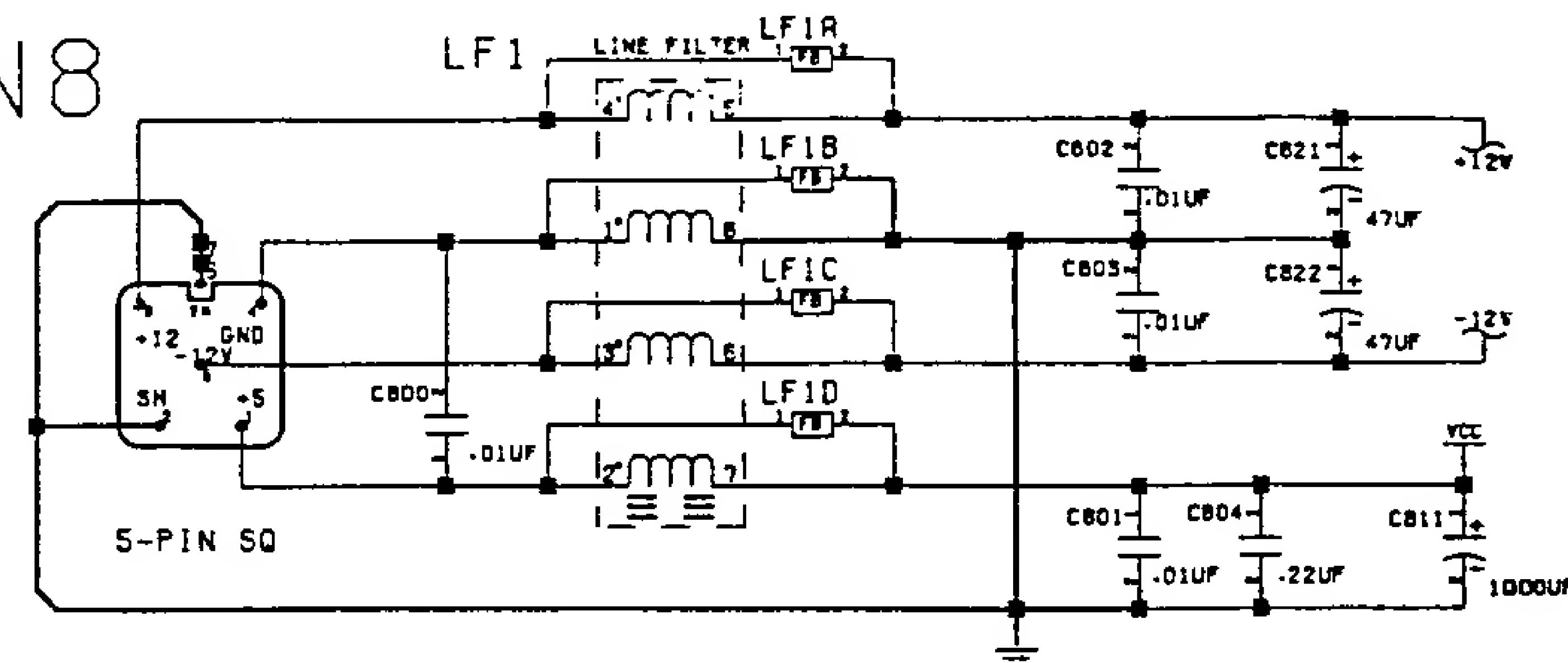


DELETED R1B PCB

P1

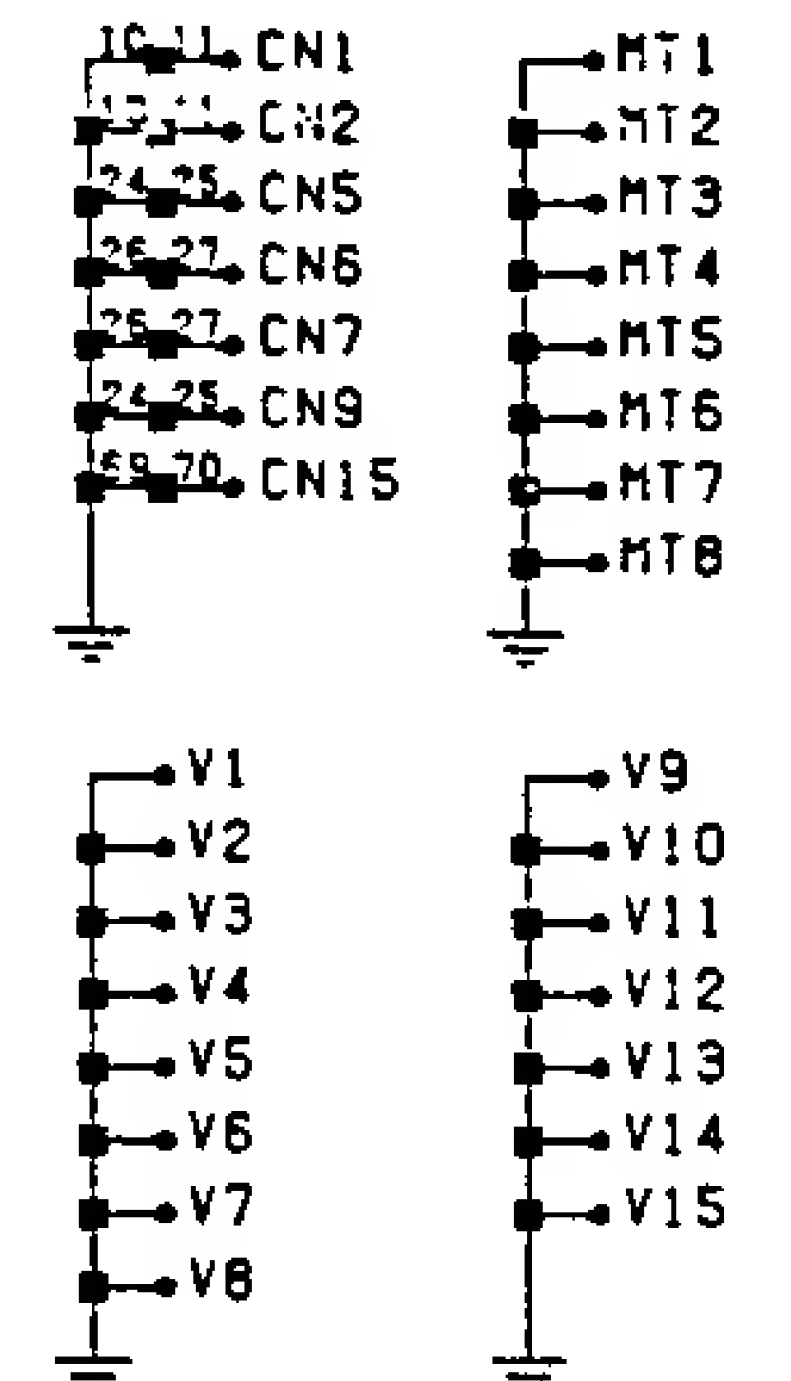
POWER INPUT

CN8

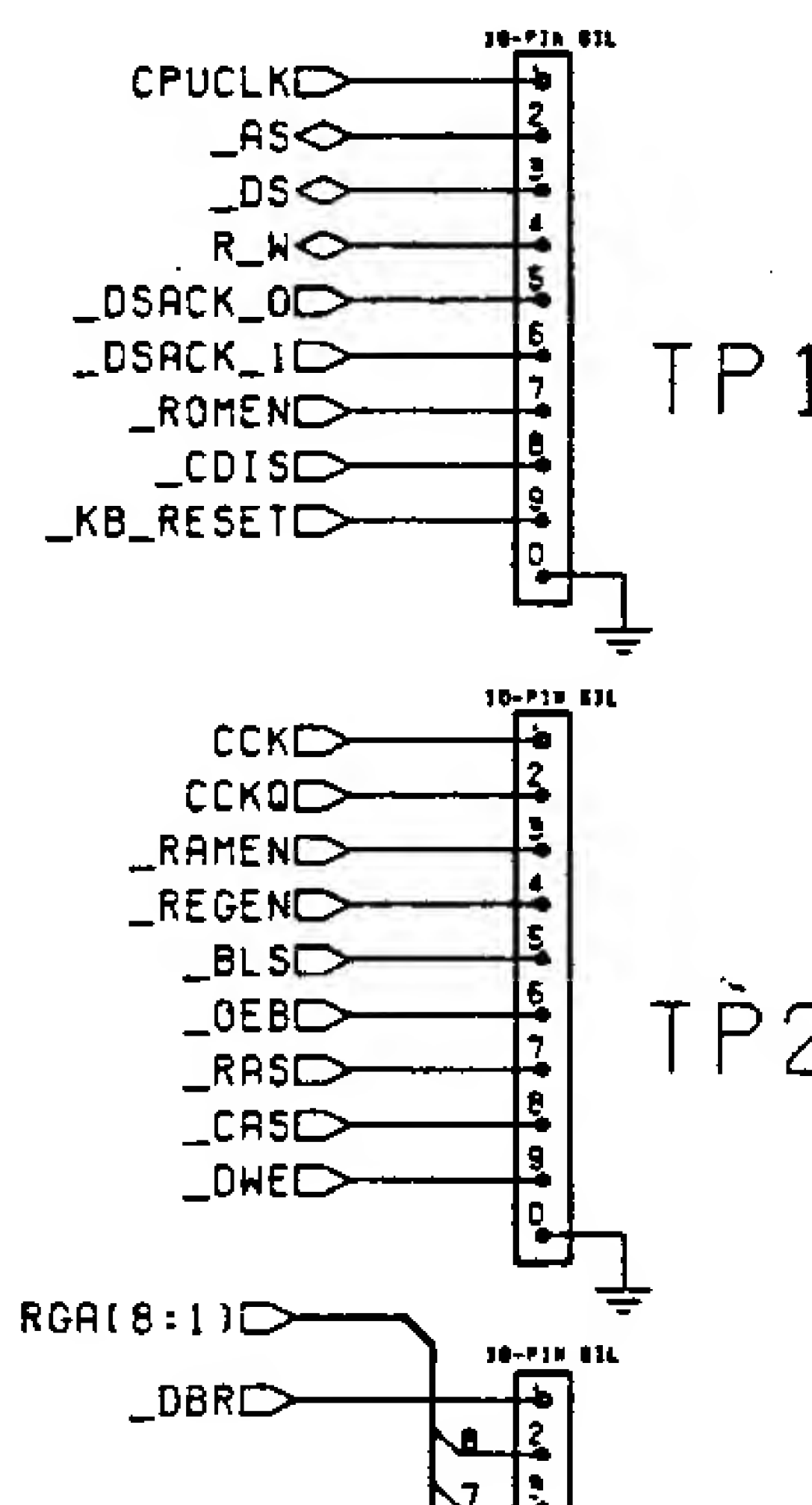


NOTE: HEAVY LINES INDICATE A SINGLE POINT CONNECTION

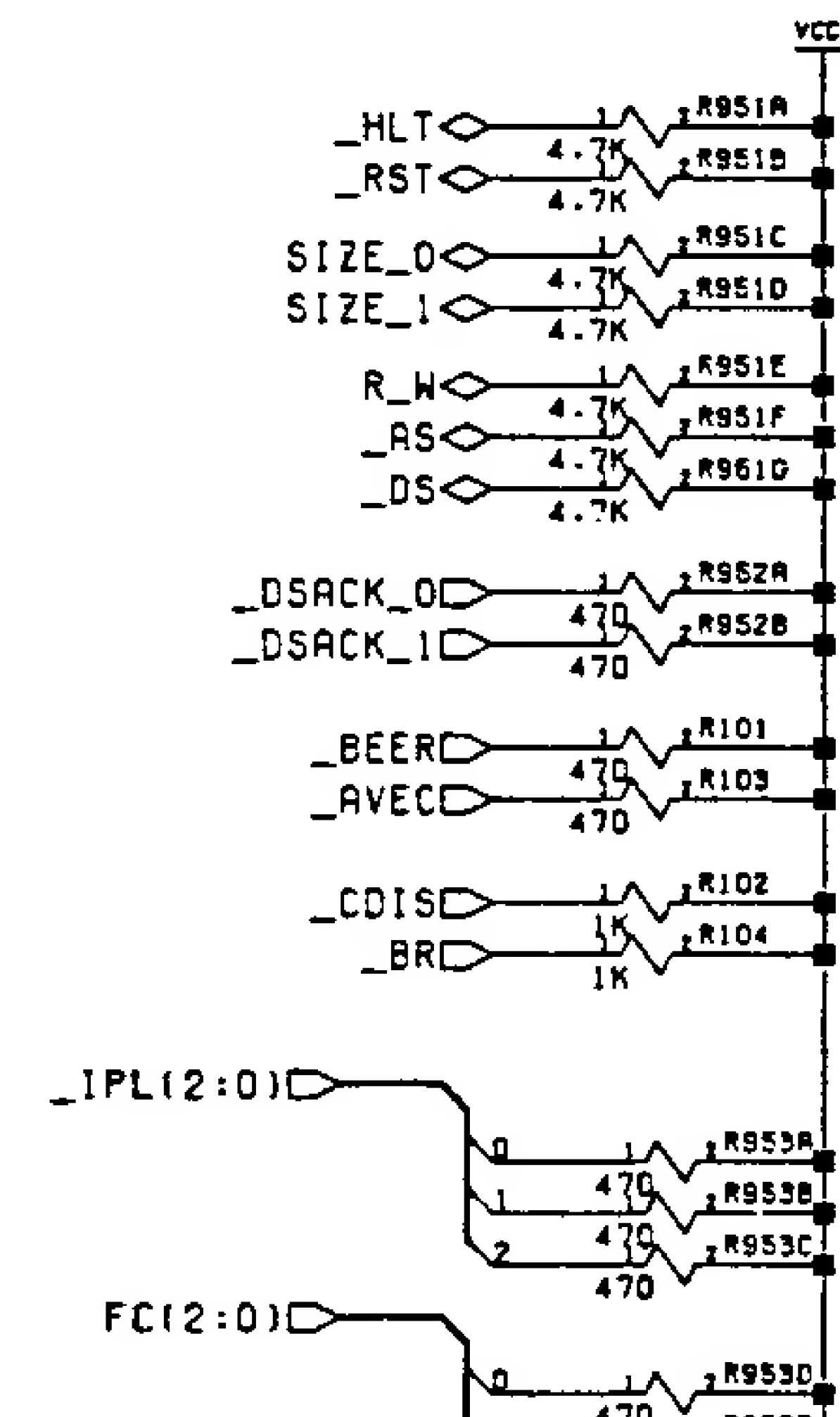
HOLES & C.



TEST ACCESS



TERMINATION



[illegible]

REF	TYPE	DESCRIPTION	PAGE
CN1	DPSP	MOUSE JOYSTICK 1	5
CN2	DPSP	MOUSE JOYSTICK 2	5
CN3	RGB-1	RIGHT AUDIO INPUT	5
CN4	RGB-2	LEFT AUDIO OUTPUT	5
CN5	DP235	EXTERNAL FLOPPY	6
CN6	DP255	S232 SERIAL PORT	7
CN7	DP255	PARALLEL PRINTER PORT	7
CN8	SO-DIN	POWER SUPPLY CONNECTOR	7
CN9	VIDEO	VIDEO	7
CN10	RGB-3	COMPOSITE VIDEO	4
CN11	DIL-34	INTERNAL FLOPPY SIGNAL	4
CN12	SIL-4	INTERNAL FLOPPY POWER	8
CN13	MCH-30	KEYBOARD MEMORAND	9
CN14	SIL-30	KEYBOARD POWER	9
CN15	MCH-30	KEYBOARD MEMORAND	9
CN14	SIL-4	KEYBOARD STATUS LEDs	9
CN15	PCMCIA	PC MEMORY CARD	11
P9		EDGE-MEMORY BUS EXPANSION	12

SIGNAL	DESCRIPTION (AREA)	PAGES
28MHz	28.63636 MHZ MASTER CLOCK	
7MHz	7.15009 MHZ PROCESSOR CLOCK	
AI(23:1)	PROCESSOR ADDRESS BUS (160000)	
ACK	DATA ACKNOWLEDGE (PARALLEL PORT)	
AS	ADDRESS STROBE (160000)	
AUDIN	AUDIO INPUT (RS232 PORT)	
AUDOUT	AUDIO OUTPUT (RS232 JACK)	
BEF	BUS ERROR (160000)	
BG	BUS GRANT (160000)	
BARBK	BUS GRANT ACKNOWLEDGE (160000)	
BLISS	BLITTER SLOWDOWN (CHIPS)	
BLIT	CHIP MEMORY ACKNOWLEDGE (CHIPS)	
BR	BUS REQUEST (160000)	
BUSY	DEVICE BUSY (PARALLEL PORT)	
CAS1/0	COLUMN ADDRESS STROBE (DRAM)	
CLK/CLKO	COLOR CLOCK / QUADRATURE (CHIPS)	
CLKC	7.15009 MHZ QUADRATURE CLOCK (CHIPS)	
CHNG	MEDIA CHANGE (FLOPPY)	
CLKO/WR	CLOCK / WRITE (RTC)	
COMP	MONOMORPHIC COMPOSITE VIDEO (VIDEO)	
CSYNC	COMPOSITE SYNC (VIDEO)	
CIS	CLEAR TO SEND (RS232 PORT)	
D15:0	PROCESSOR DATA BUS (160000)	
DIR	STEP DIRECTION (FLOPPY)	
DR0	DISK READ (FLOPPY)	
DRW	DISK WRITE DATA (FLOPPY)	
DRWE	DISK WRITE ENABLE (FLOPPY)	
DREQ	CHIP DMA REQUEST LINE (CHIPS)	
DRB:0	DRAM ADDRESS BUS (DRAM)	
DRB:15:0	DRAM DATA BUS (DRAM)	
DSR	DATA SET READY (RS232 PORT)	
DTACK	DATA TRANSFER ACKNOWLEDGE (160000)	
DIR	DATA TERMINAL READY (RS232 PORT)	
E	PERIPHERAL ENABLE CLOCK (160000)	
EXTICK	EXPANSION PRESENT / RTC TICK	
EXP2:0	FUNCTION CODE (160000)	
EXP2:0	EXP. FUNCTION 0/1 (TICKS)	
HZ	PROCESSOR HZ (160000)	
HSYNC	HORIZONTAL SYNC (VIDEO)	
INDEX	INDEX PULSE (FLOPPY)	
INT(2,3,6)	INTERRUPT REQUEST (CHIPS)	
IORESST	I/O RESET	
IP1(2:0)	INTERCEPT PRIORITY LEVEL (160000)	
KBLOCK	KEYBOARD CLOCK (KEYBOARD)	
KEYDATA	KEYBOARD DATA (KEYBOARD)	
KRESST	KEYBOARD RESET (KEYBOARD)	
LDS/UDS	UPPER / LOWER DATA STORES (160000)	
LFD	POWER ON / FLD / AUDIO FILTER DISABLE	
LEFT/RIGHT	LEFT / RIGHT AUDIO (AUDIO)	

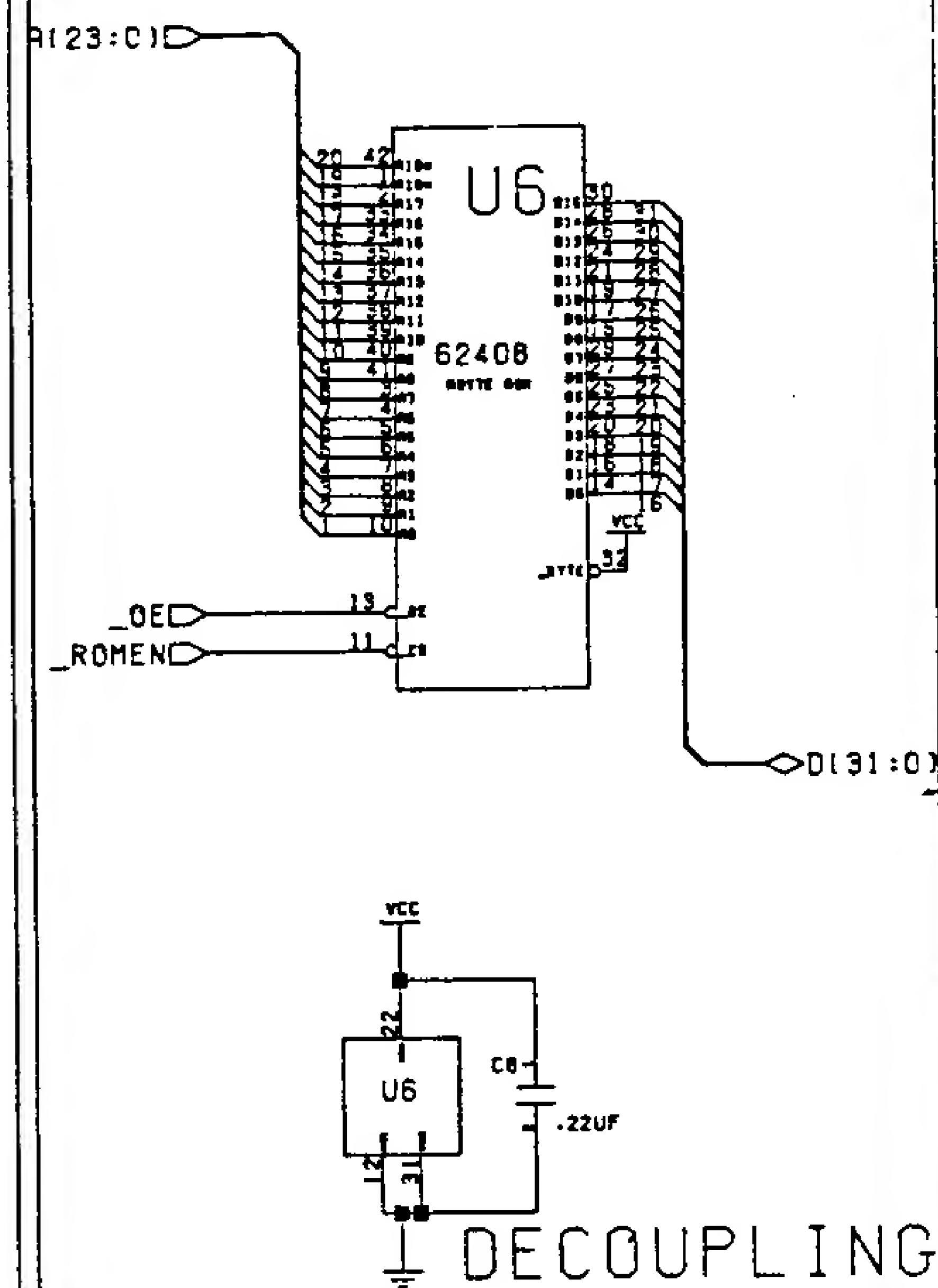
SIGNAL	DESCRIPTION (AREA)	PAGES
LFPN	LIGHT PEN TRIGGER (JOYSTICKS)	
MTR	MOTOR ON (FLOPPY)	
MIRD	MOTOR ON - DRIVE 0 (FLOPPY)	
MOV/ROM	MOUSE 0 QUADRATURE V/H (JOYSTICKS)	
MV/MIM	MOUSE 1 QUADRATURE V/H (JOYSTICKS)	
OV	OVERRIDE / OVER RESET	
OVR	OVERRIDE SYSTEM DECODING	
PIXELSW	GENLOCK PIXEL SWITCH (VIDEO)	
POTX/OV	POT LINES 0 X/Y (JOYSTICKS)	
PCTIX/LY	POT LINES 1 X/Y (JOYSTICKS)	
PRDT	PAPER OUT (PARALLEL PORT)	
PRDT(2:0)	PARALLEL PORT DATA (PARALLEL PORT)	
RCHN	ROM ENABLE (CHIPS)	
REGEN	CHIP REGISTER ENABLE (CHIPS)	
RESO/1	ROW ADDRESS STROBE (DRAM)	
RHY	DRIVE READY (FLOPPY)	
RESET	GENERAL RESET	
RS28A(1)	REGISTER ADDRESS BUS (CHIPS)	
R/G/R	RED / GREEN / BLUE (VIDEO)	
RI	RING INDICATE (RS232 PORT)	
RCHN	ROM ENABLE (ROM)	
RS	REQUEST TO SEND (RS232 PORT)	
RST	PROCESSOR RESET (SCANDIO)	
RVD	RECEIVE DATA (RS232 PORT)	
RA	PROCESSOR READY (SCANDIO)	
SEL	SELECT (PARALLEL PORT)	
SEL(3:0)	DRIVE SELECT (FLOPPY)	
SIDE	SIDE SELECT (FLOPPY)	
STEP	STEP IN/OUT COMMAND (FLOPPY)	
TRCK	TRACK ZERO SENSE (FLOPPY)	
TXD	TRANSMIT DATA (RS232 PORT)	
VHA	VALID MEMORY ADDRESS (6800A)	
VPA	VALID PERIPHERAL ADDRESS (68000)	
VSYNC	VERTICAL SYNC (VIDEO)	
WE	WRITE ENABLE (DRAM)	
WRST	WRITE PARTIC SENSE (FLOPPY)	
XCLK	EXTERNAL CLOCK (VIDEO)	
XCLKEN	EXTERNAL CLOCK ENABLE (VIDEO)	
XRDY	EXTERNAL DATA READY	

== CREDIT CARD AND IDE STUFF? ==

[illegible]

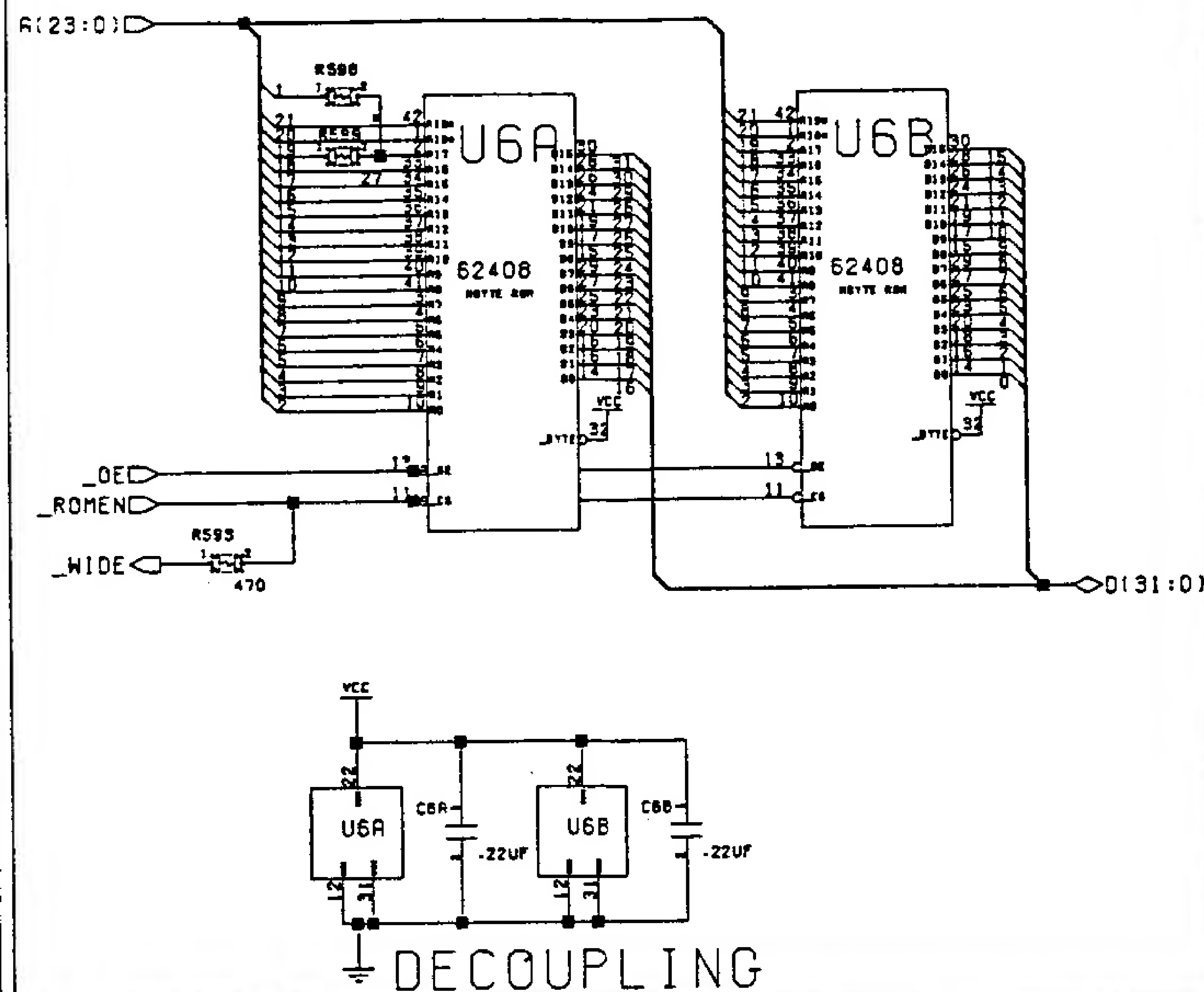
REF	CHIP	DESCRIPTION	PAGE
U1	8A000	85000 PROCESSOR 16MHZ	2
U2	8374	RA1 (CF (AP GENUS)	2
U3	8364	RA1A	5
U4	4203	PLA (AA DENISE)	4
U5	F023A	RA1 GAYLE (CAH ASIC)	2.8
U7	8511	ROT 512X16 (50 NS)	10
U7-B	B520	RAM (C) 1 MB	7
U10-1	28F10	FLASH MEMORY 128KX8	10
U12	8511	RAM 512X16	10
U13	8AC05	RAM (A) KEYBOARD MP1	4
U14	PS1516	LOW VOLTAGE SENSE IC	9
U15	LF947	BIOS OF-RMP	5
	LF944	BIOS OF-RMP	INT
U16-17	8511	RAM 256X16	8
U18-19	8511	RAM 256X16 OPTIONAL	8
U20	981277	BUGGLE (ASIC)	2
U28	168A	EIA LINE DRIVER	7
U29	1489	EIA LINE RECEIVER	7
U30	AT101	TRIPLE B-BIT VIDEO GAC4	14
X1	OSC	11L 28.63636 MHZ NSIC 12	1
	OSC	11L 28.32512 MHZ PA1	INT
Y451	XTAL	4.24518 MHZ PA1 BURST	14
Y621	XTAL	3MHZ CERRANT R50NATO959	14
X2	BS51	PA1 VIDEO MODULATOR	14
	BS51	NSIC VIDEO MODULATOR	14

16-BIT ROM



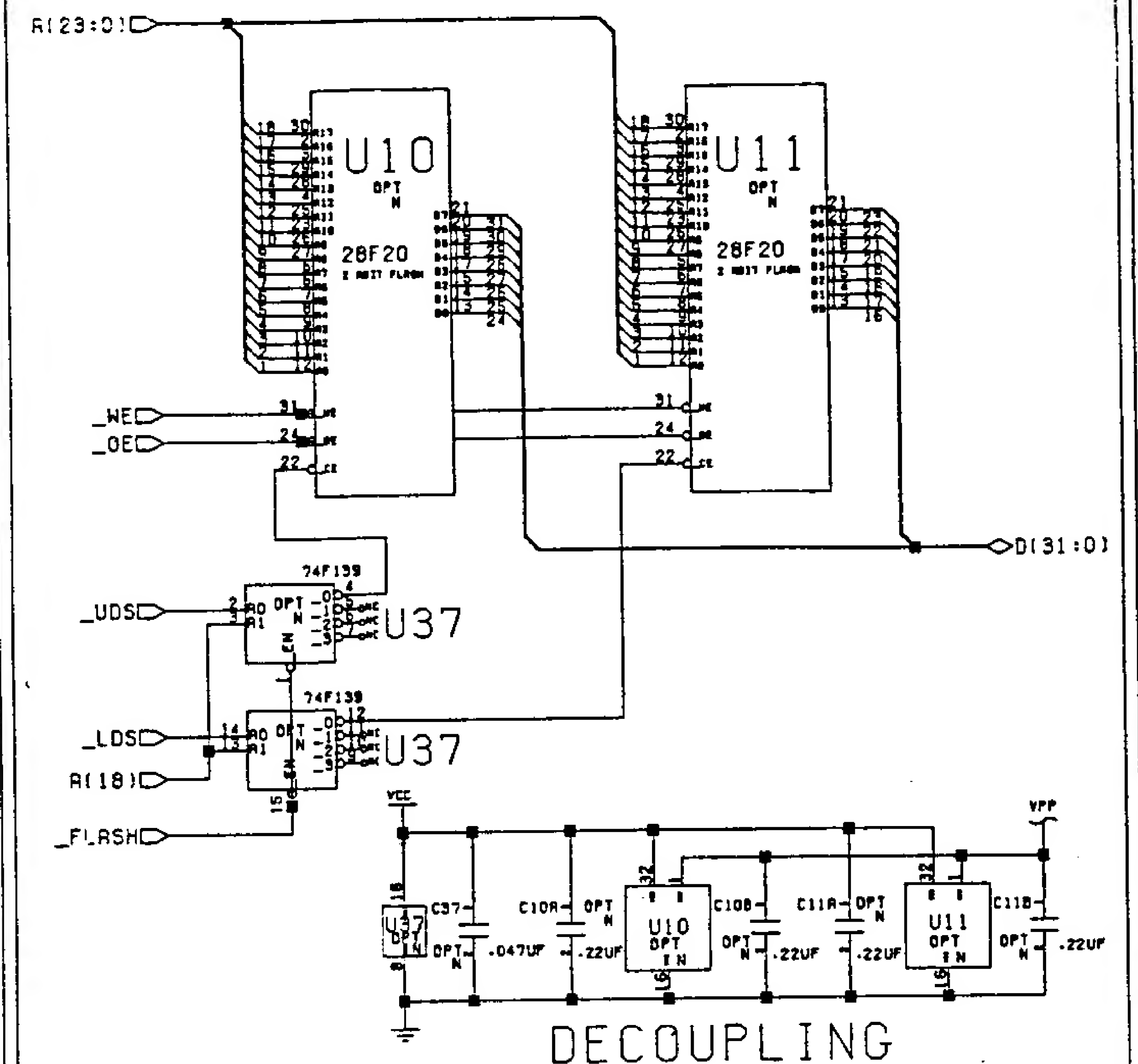
IF ROM16="YES"

32-BIT ROM



IF ROM32="YES"

FLASH MEMORY

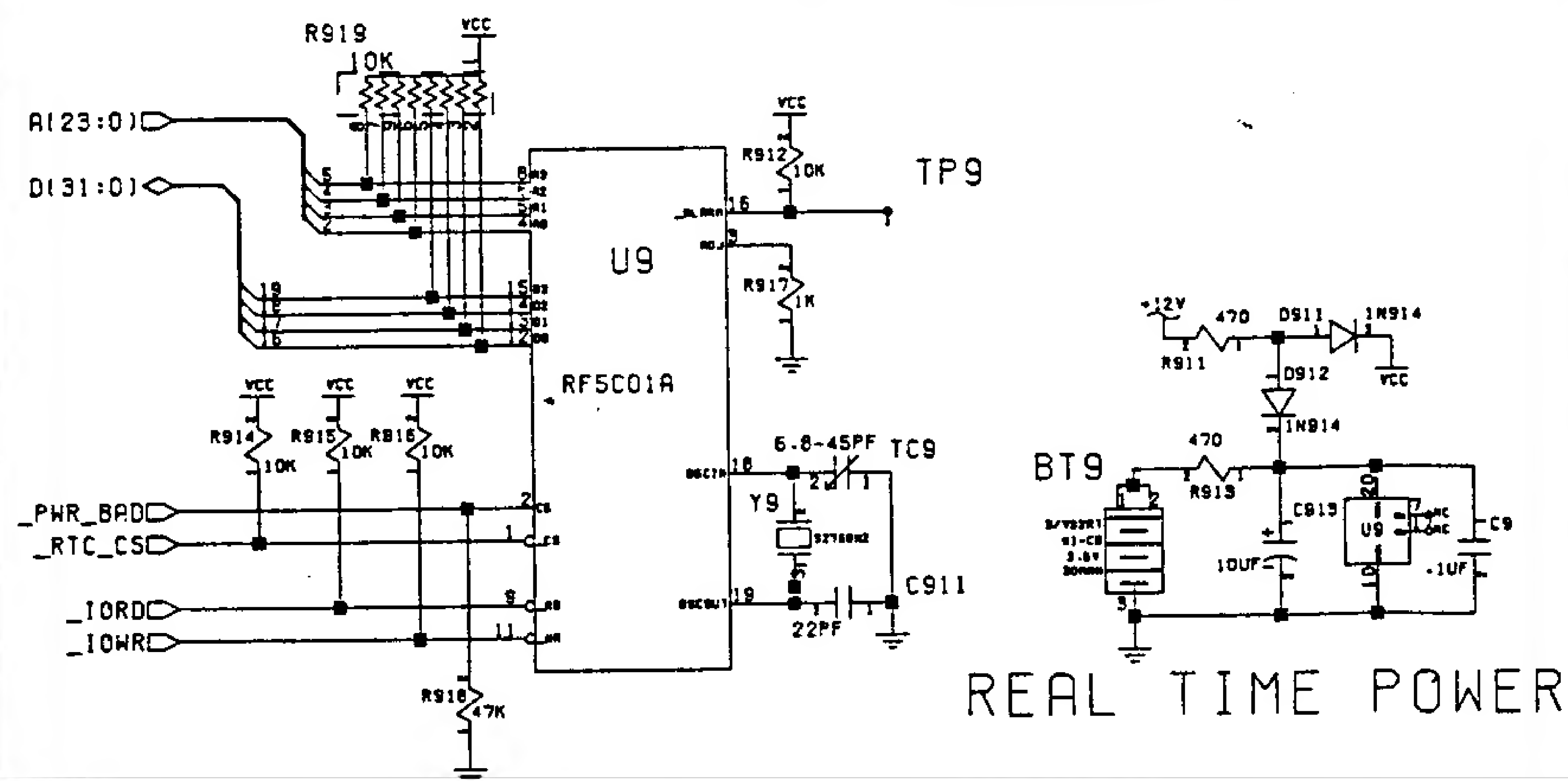


IF FLASH="YES"

16 AND 32-BIT SOCKETS MAY OVERLAP!

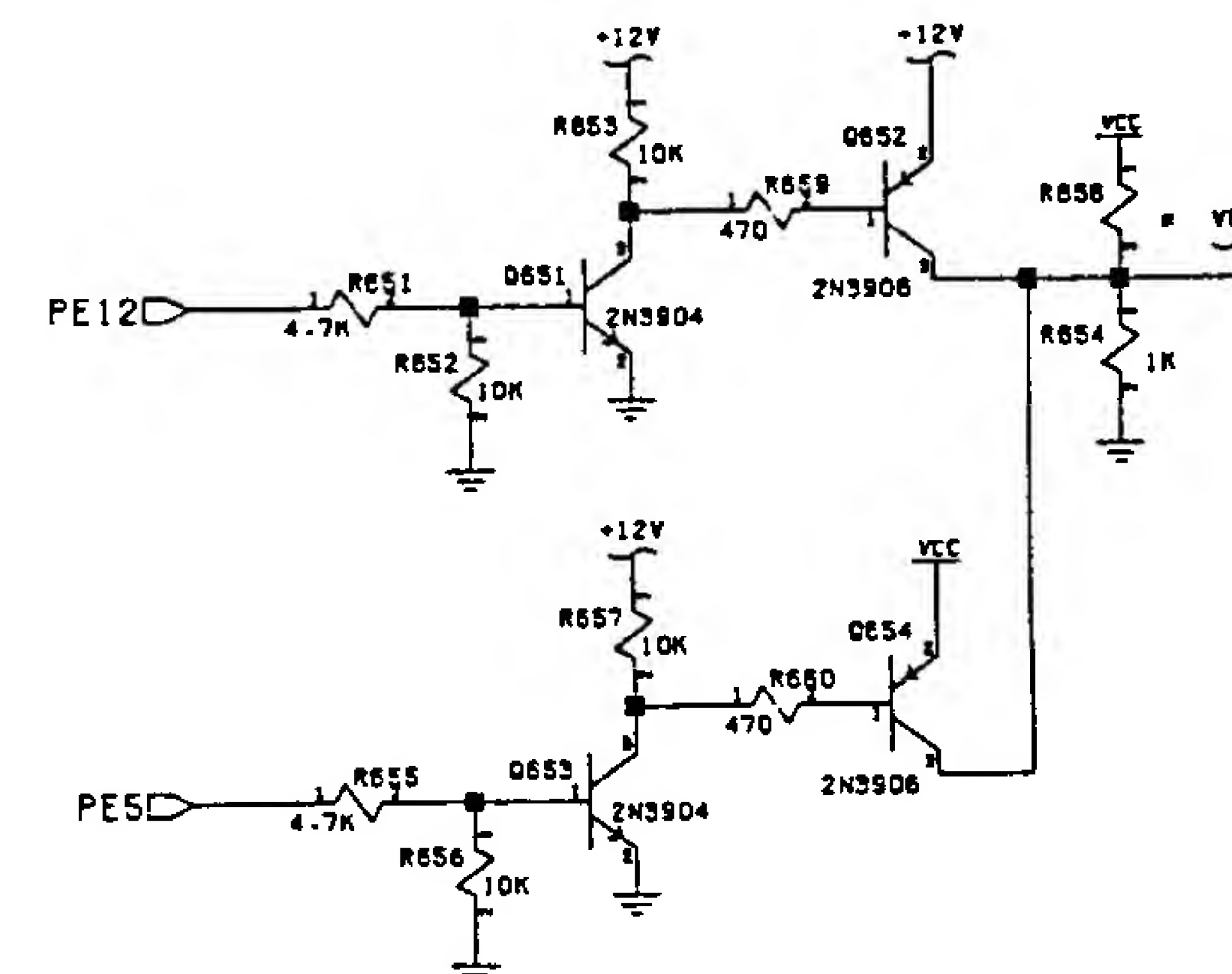
OPTIONAL FLASH MEMORY

REAL TIME CLOCK



IF RTC="YES"

PROGRAMMING VOLTAGE



OPTIONAL REAL-TIME CLOCK/CALENDAR

A1200 REV 1->1D PCB

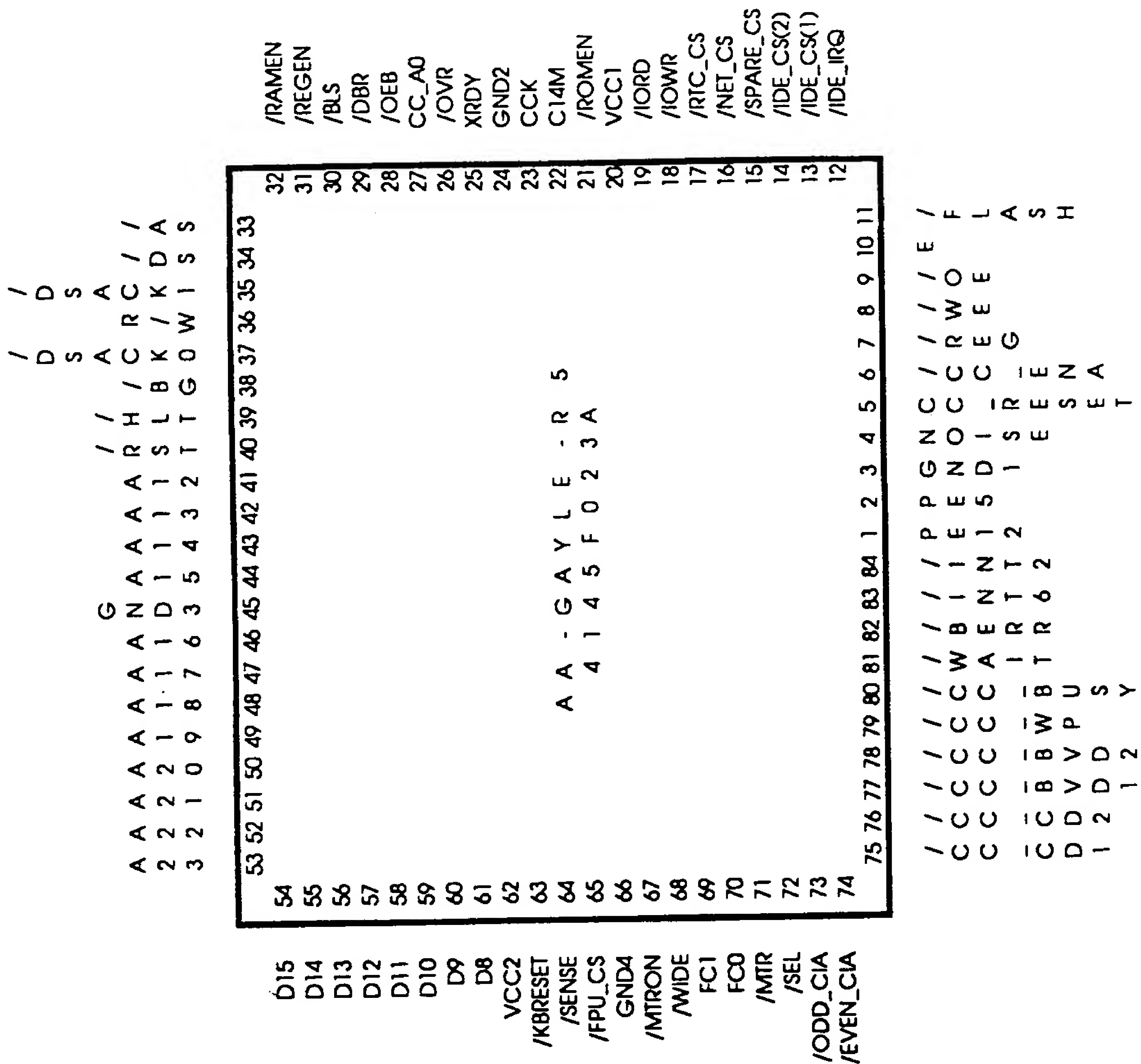
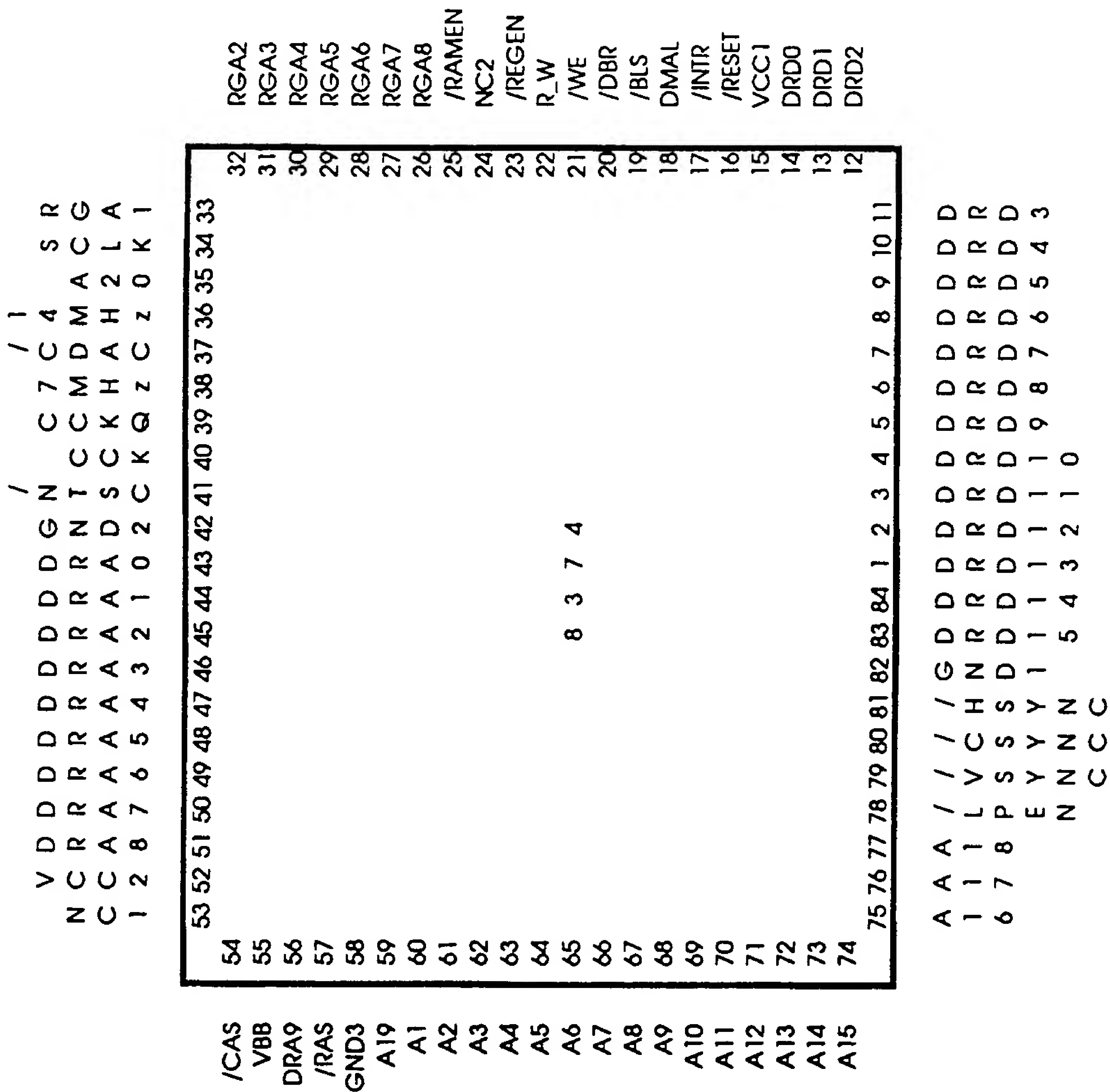


Figure 5-5. AA-GAYLE



[illegible]

	53	52	51	50	49	48	47	46	45	44	43	42	41	40	39	38	37	36	35	34	33
54																					
55																					
56																					
57																					
58																					
59																					
60																					
61																					
62																					
63																					
64																					
65									4	2	0	3									
66																					
67																					
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73																					
74																					
	75	76	77	78	79	80	81	82	83	84	1	2	3	4	5	6	7	8	9	10	11

R5
R6
VCC2
R7
/BRST
D31
D30
D29
D28
D27
D26
D25
D24
D23
D22
D21
D20
D19
D18
D17
D16

B2
B1
VCC1
B0
ZD
BLANK
SOG
C28M
/RST
C14O
SCLK
/MLD
MDAT
RGA1
RGA2
RGA3
RGA4
RGA5
RGA6
RGA7
RGA8

D	D	D	D	D	D	D	D	V	D	G	D	D	D	D	D	/	C W
1	1	1	1	1	9	8	C	7	N	6	5	4	3	2	1	C	C I
5	4	3	2	1	0		C		D							A	K D
							3		I							S	E

CHANNEL Z

10/10/92

D181:0
A123:0
FC12:0

_CDIS
_BOSS
_BR
_BG

_IPL12:0

CPUCLK

_AVEC
_BEER
_DSACK_O
_DSACK_I

_RMC
_R_N
_RS
_DS
_SIZE_O
_SIZE_I
_RST
_HLT

_FPU_SENSE

_FPU_CS

_DSACK_O
_DSACK_I
_R_N
_RS
_DS
_RESET
_HLT

_DSACK_O
_DSACK_I
_R_N
_RS
_DS
_RESET
_HLT

_DSACK_O
_DSACK_I
_R_N
_RS
_DS
_RESET
_HLT

_DSACK_O
_DSACK_I
_R_N
_RS
_DS
_RESET
_HLT

_DSACK_O
_DSACK_I
_R_N
_RS
_DS
_RESET
_HLT

_DSACK_O
_DSACK_I
_R_N
_RS
_DS
_RESET
_HLT

_DSACK_O
_DSACK_I
_R_N
_RS
_DS
_RESET
_HLT

_DSACK_O
_DSACK_I
_R_N
_RS
_DS
_RESET
_HLT

_DSACK_O
_DSACK_I
_R_N
_RS
_DS
_RESET
_HLT

_DSACK_O
_DSACK_I
_R_N
_RS
_DS
_RESET
_HLT

_DSACK_O
_DSACK_I
_R_N
_RS
_DS
_RESET
_HLT

_DSACK_O
_DSACK_I
_R_N
_RS
_DS
_RESET
_HLT

U1

MC68E020

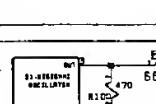
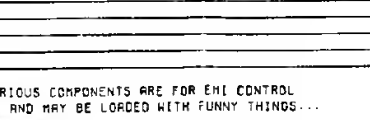
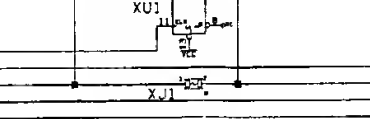
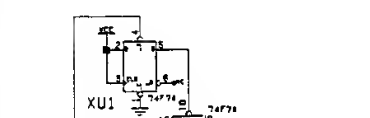
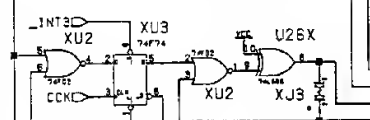
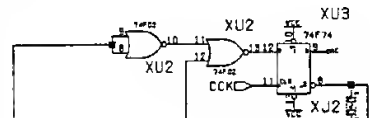
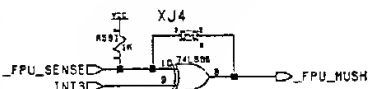
U0

MC68881

OOPS!

ALL THIS STUFF SERVES TO CORRECT ONE MINOR BYTE BUG AND A NUMBER OF ALICE DEFICIENCIES NOT YET CORRECTED. ASSUMING THE CHANGES ARE IMPLEMENTED IN THE PRODUCTION GAULE CHIP, THEN ALL THIS STILLNESS IS BEST ERASED...

REFERENCE	OLD GAULE	NEW GAULE
XU1	74F74	NONE
XU2	74F02	NONE
XU3	74F74	NONE
U26X	74F86	NONE
XJ4	0 OHM	OUT
XJ1	OUT	0 OHM
XJ2	OUT	0 OHM
XJ3	OUT	0 OHM
NOTE		74LS66



NOTE: PAL USES 28.37618 MHZ

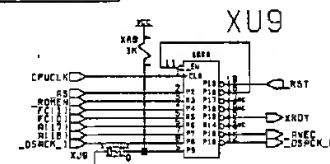
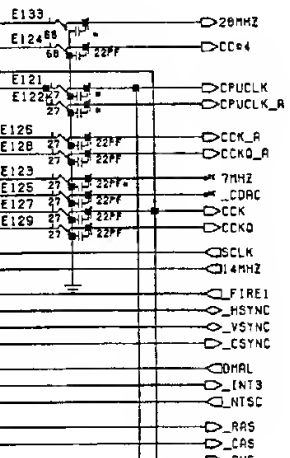
U2

8374 ALICE

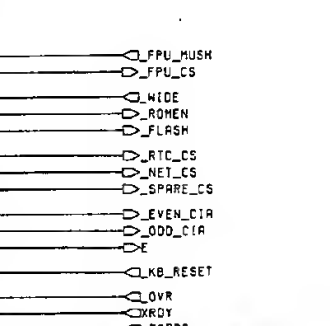
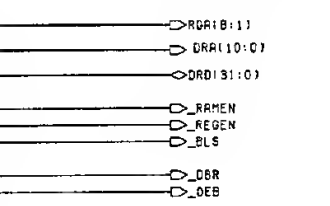
U5

4145F023A RA_GAYL

CONTROL FUNCTIONS



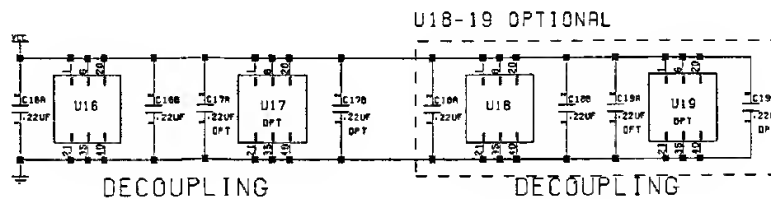
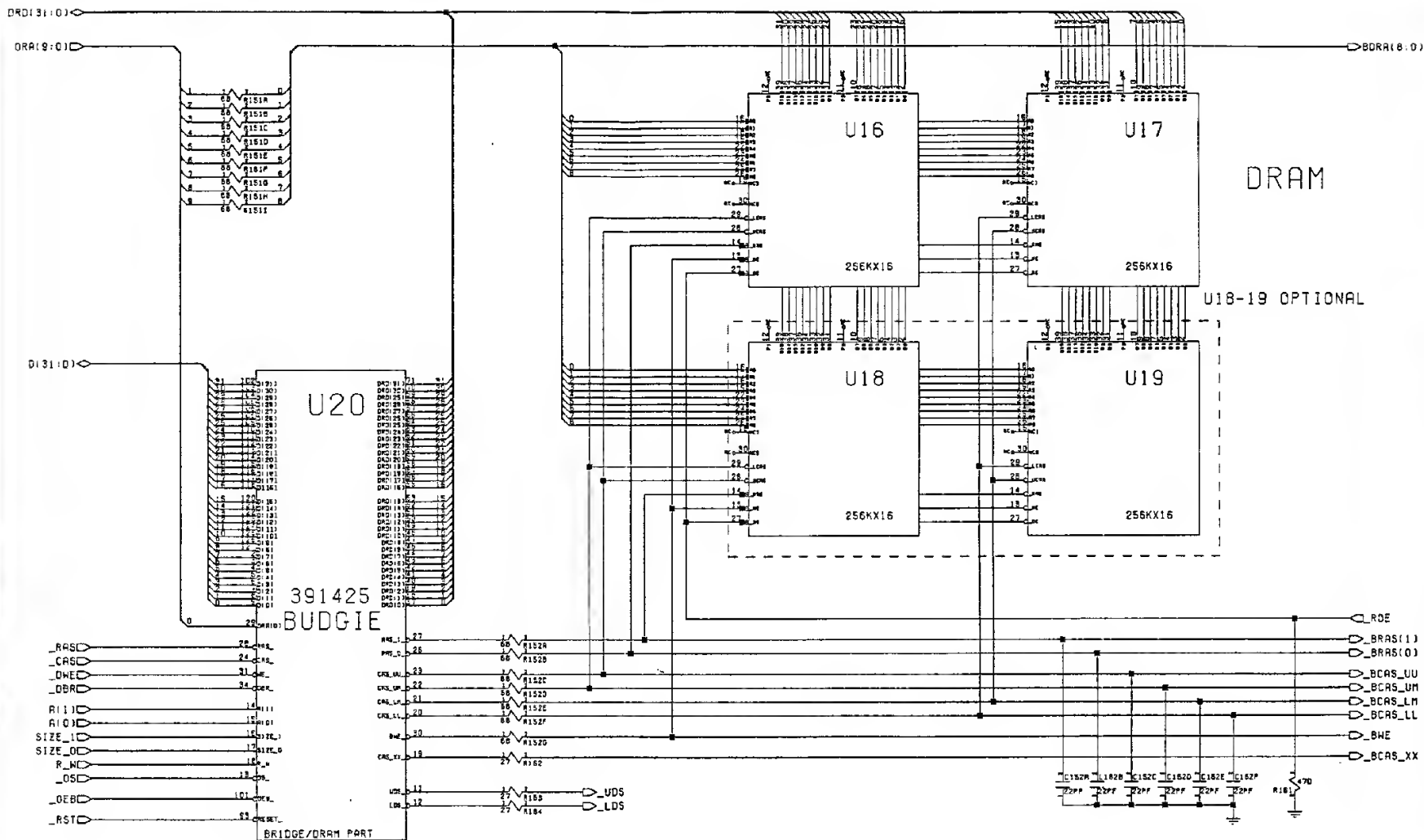
OOPS! - 32-BIT / SLOW ROM
XJ9 IN - ADD ROM WAIT



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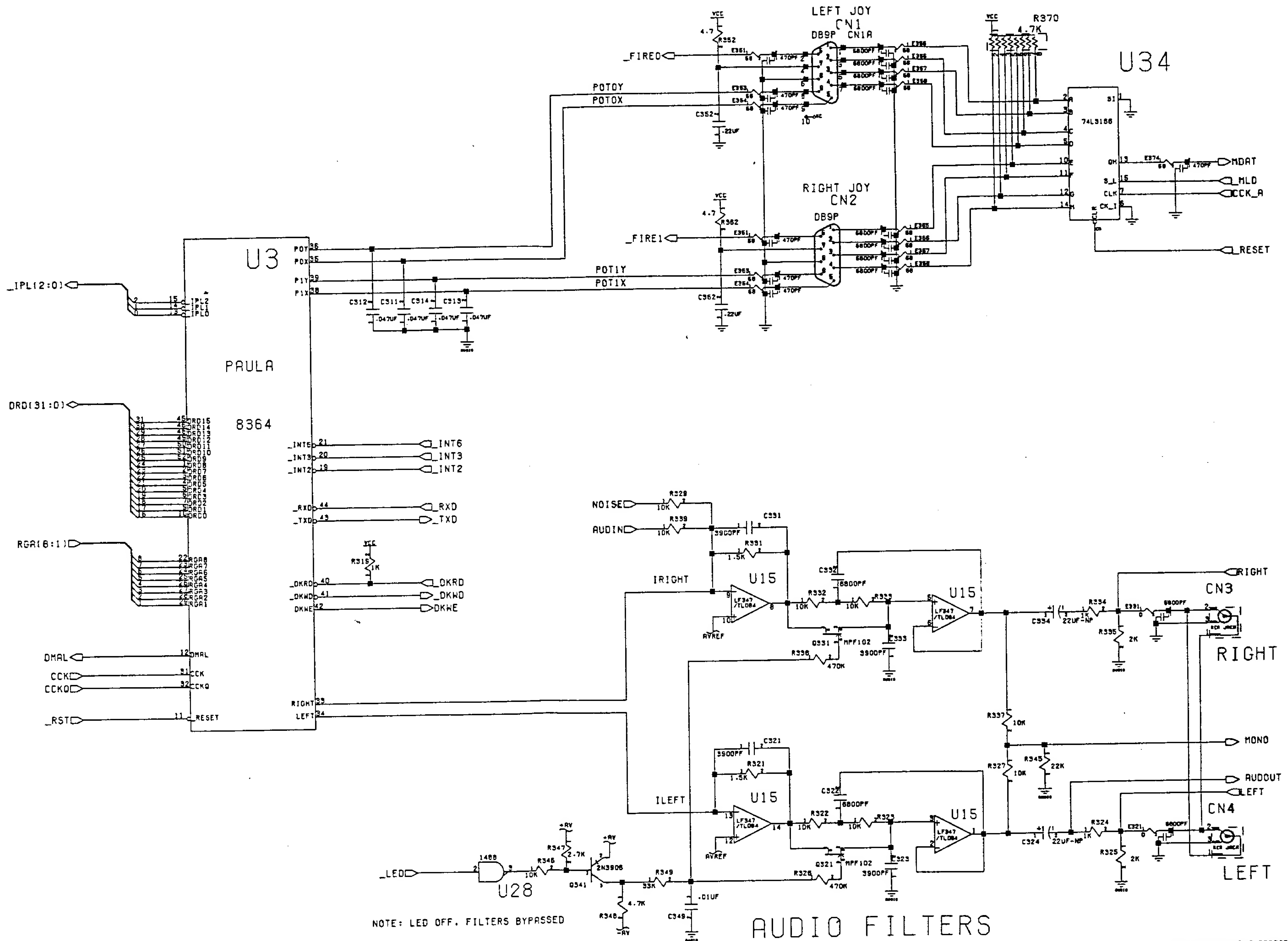
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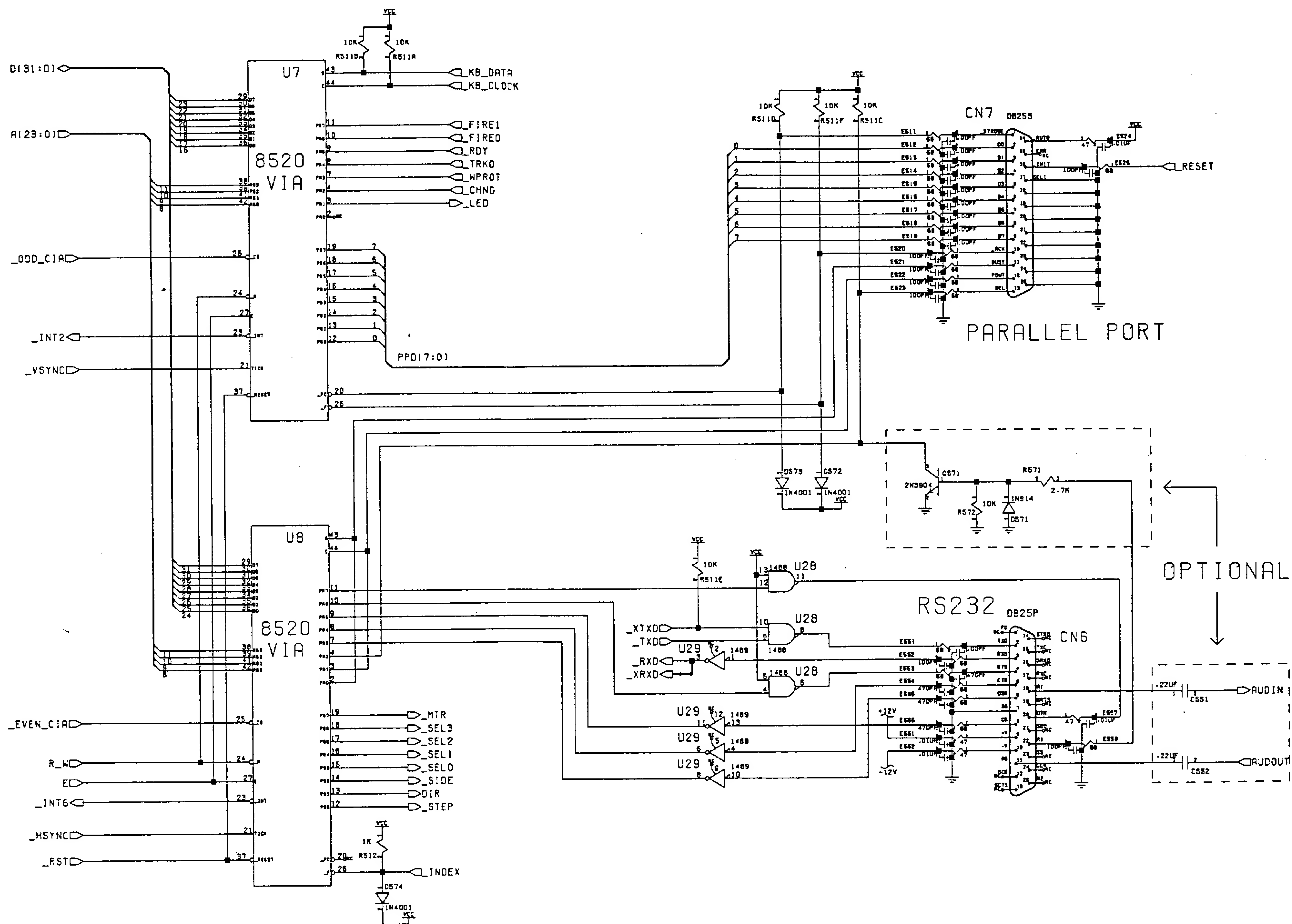


A1200 REV 1->1D PCB

MOUSE/JOYSTICK PORTS



A1200 REV 1->1D PCB



A1200 REV 1->10 PCB